

ZZZ1  
PCB DAZ@  
DAZ14200100  
LA-A401P/LS-A401P

ZZZ2  
PCB DA1@  
DA100000000  
MB LA-B051P REV01

ZZZ3  
PCB DA2@  
DA4001UK000  
P sensor/B LS-B051P REV01

S1  
ZSJVV\_MB\_FRAME 1 PC A false  
EC10G000200  
@

# Compal Confidential

## Schematics Document

ZSJ2V

LA-B051P

www.rosefix.com

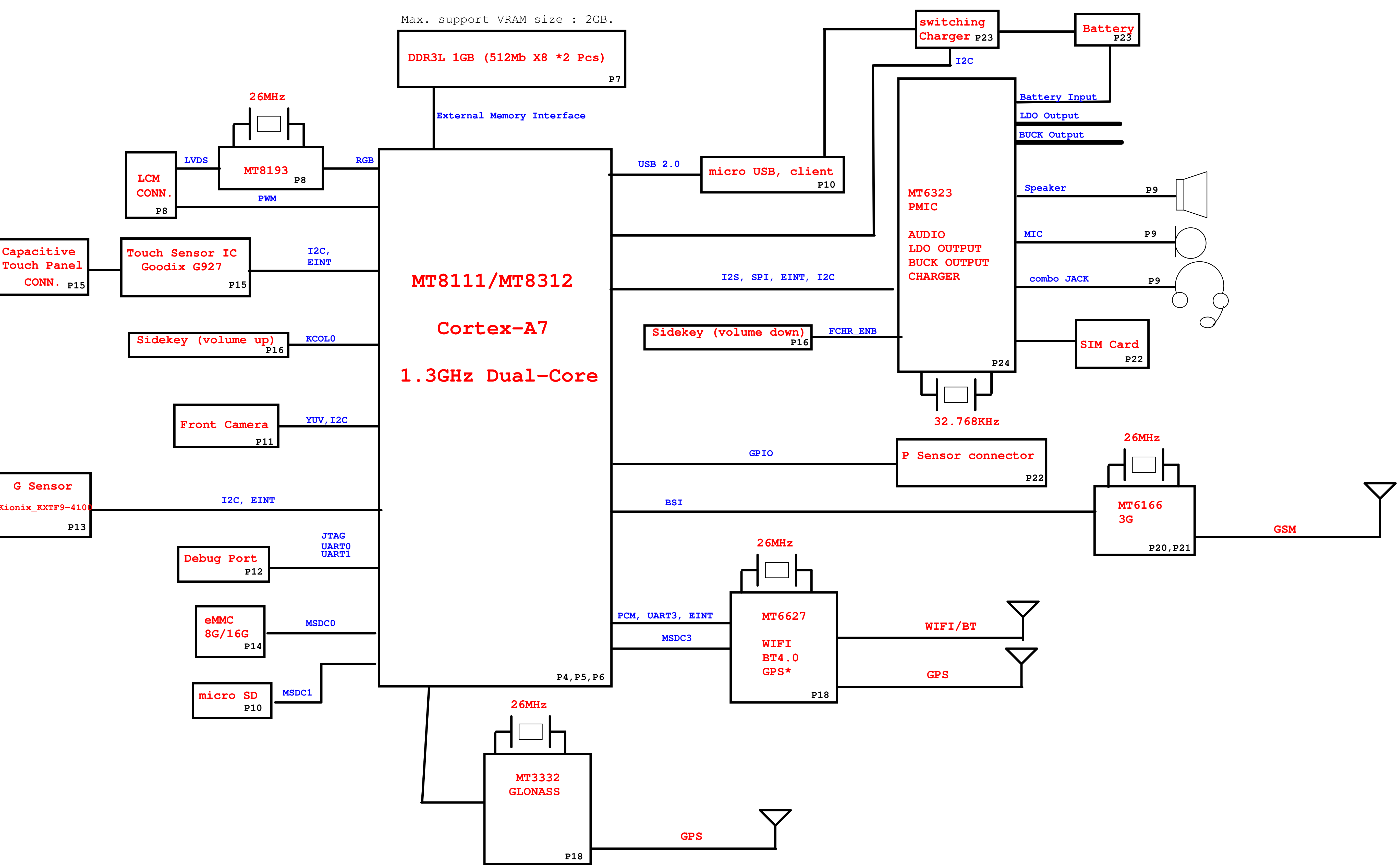
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2013-09-17D

REV:0.1

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giordano3 block diagram



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				Block Diagram
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Voltage Rails

MT6323 Power Plane	Function	Power Level	IDLE	Sleep mode
VPROC_PMU	VPROC switching output	0.7 ~ 1.4V	ON	Low voltage
VTCXO_PMU	LDO output for TCXO	2.8V	ON	OFF
VCAMA_PMU	LDO output for camaera analog	2.8V	ON	Gating by SW
VIO28_PMU	LDO output used for 2.8V IO	2.8V	ON	Gating by SW
VCAMD_IO_PMU	LDO output for camaera 1.8V	1.8V	ON	Gating by SW
VEMC_3V3_PMU	LDO output for eMMC&P-sensor	3.3V	ON	Gating by SW
VMCH_PMU	LDO output for SD card	3.3V	ON	Gating by SW
VGP2_PMU	LDO output for Touch panel	2.8V	ON	Gating by SW
DDR3VCCIO	LDO output for DDR3L	1.35V	ON	Gating by SW
VGP3_PMU	Bridge power	1.2V	ON	Gating by SW
VIO18_PMU	LDO output used for 1.8V IO	1.8V	ON	Gating by SW
VIO18_RF	LDO output for RF_MT6166	1.8V	ON	Gating by SW
WB_AVDD33	LDO output for RF_MT6627	3.3V		
GPS_AVDD18	LDO output for RF_MT6628	1.8V		
WB_AVDD18	LDO output for RF_MT6629	1.8V		

Main board ID

MB_ID0	MB_ID1	MB_ID2	
			EVT
			DVT
			PVT
			MP

I2C address

Device	Address(7 bit)	Address(8bit)	
		Write	Read
G-sensor (KXTIK-1004)	0x0F	0x1E	0x1F
G-sensor(STK8313)	0x22	0x44	0x45
Camera (OV7675)	0x21	0x42	0x43
Charge (FAN54015 )	0x6A	0xD5	0xD4
Touch screen (GT927)	0x5D	0xBA	0xBB

BOM structure

Name	Function	
3G@	3G only	
EMC@	for EMC/ESD/RF request	
KIONIX@	G-sensor for Kionix	
STK@	G-sensor for Sensortek	
GYRO@	GYRO	
NAN 256Mx16@	NANYA 256M(DDR3L)	
HYN 256Mx16@	HYNIX 256M(DDR3L)	
P_SENSOR@	P-sensor	
HYN 8GB@	HYNIX 8G(EMMC)	
HYN 16GB@	HYNIX 16G(EMMC)	
KIN 8GB@	Kinggston 8G(EMMC)	
KIN 16GB@	Kinggston 16G(EMMC)	
SAM 8GB@	Samsung 8G(EMMC)	
SAM 16GB@	Samsung 16G(EMMC)	
CONN@	Connector	
ME@	CLIP	
DAZ@	PCB	
GPS@	for without MT3332 SKU	

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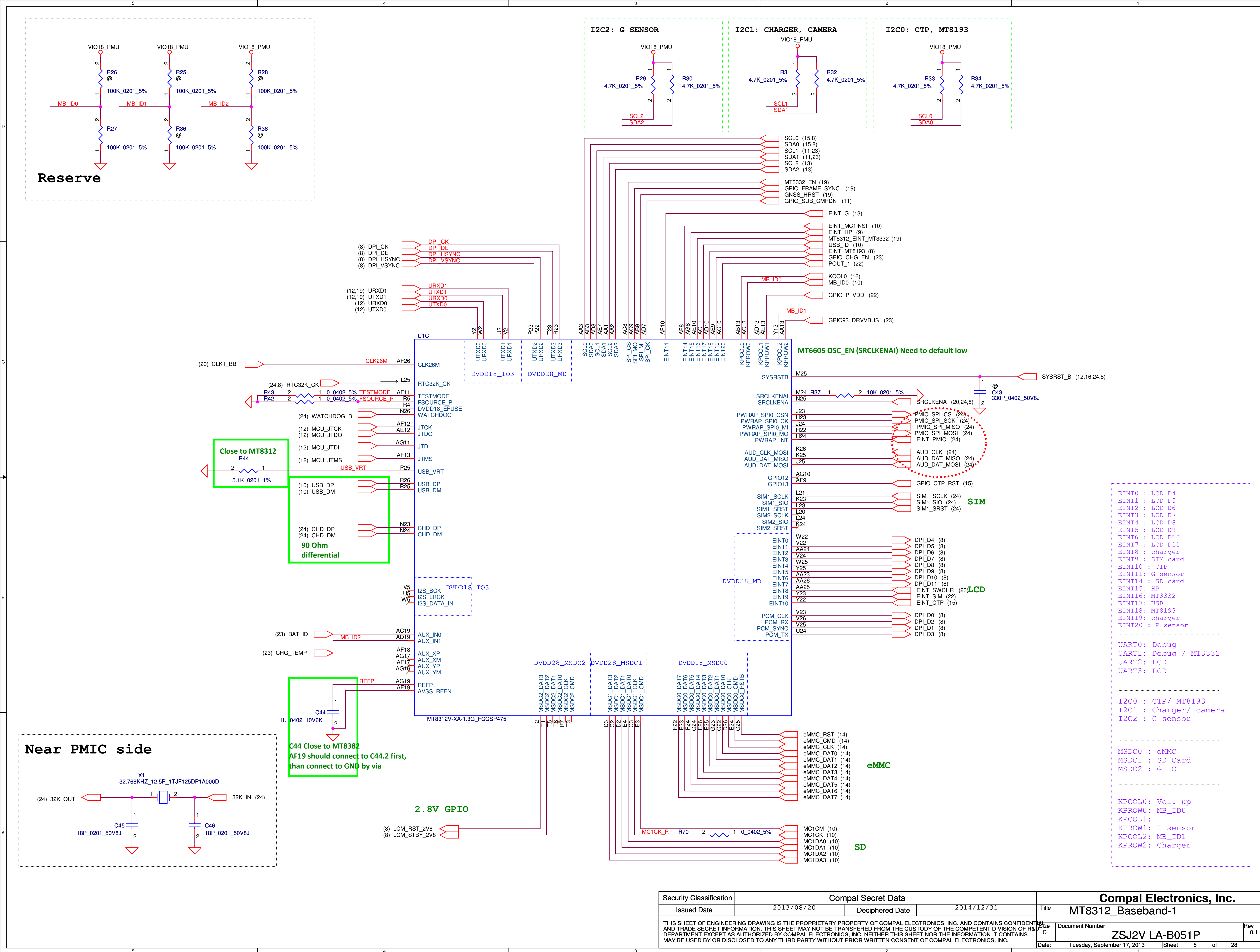


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AVDD18\_MEMPLL pin and AVSS18\_MEMPLL pin should be connected with PCB CAP first, and then connected with PMU and PCB ground. CAP should be near MT8312 as possible as we can.

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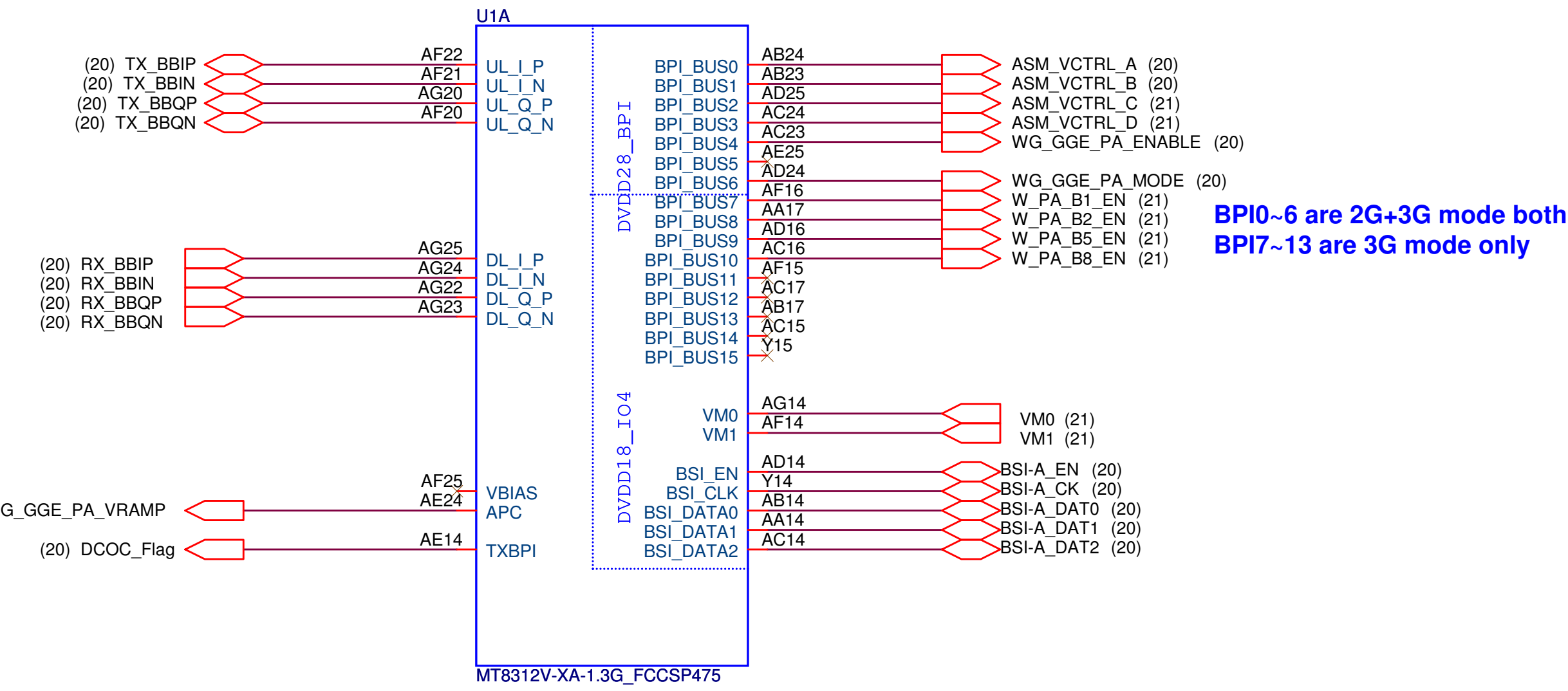


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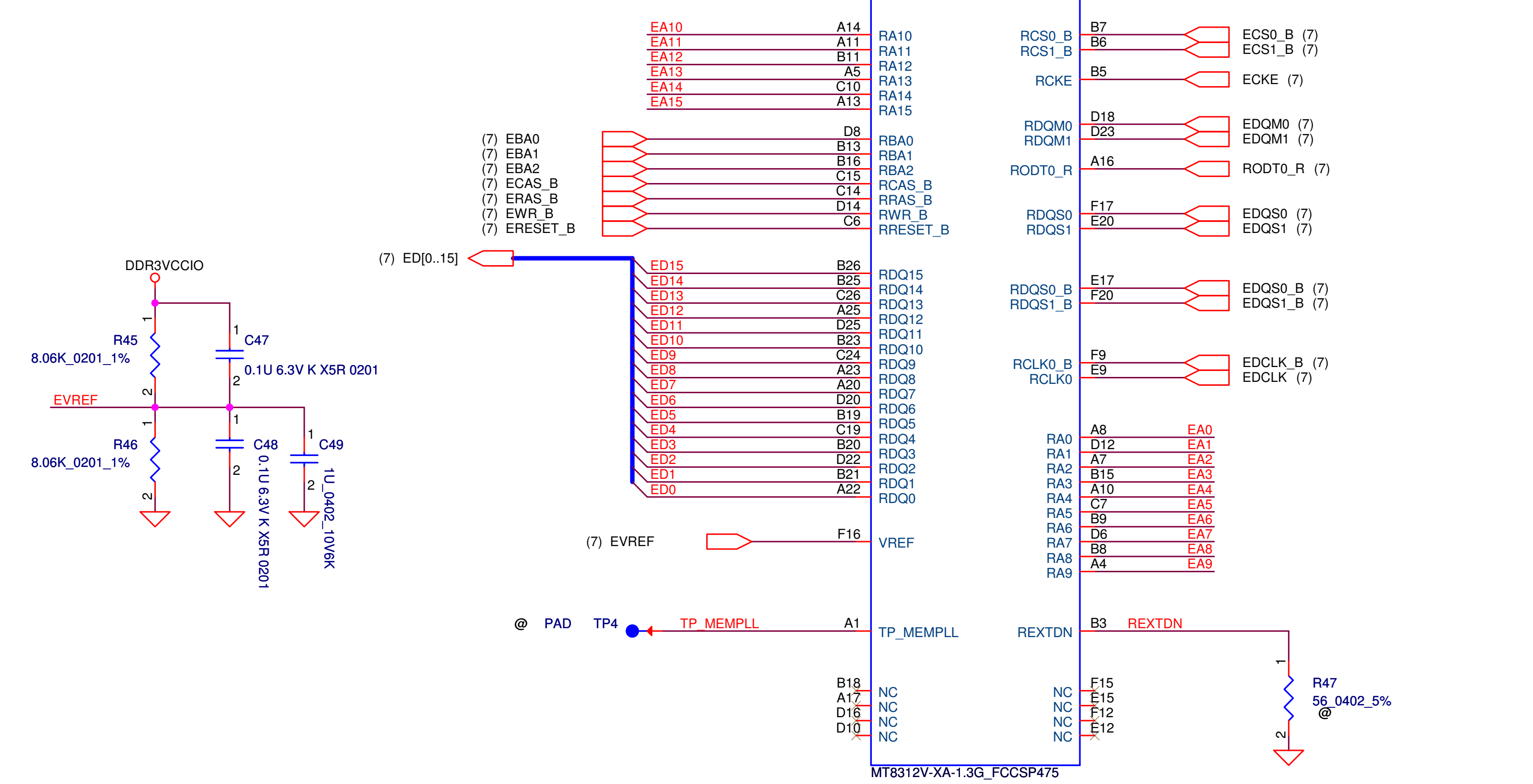
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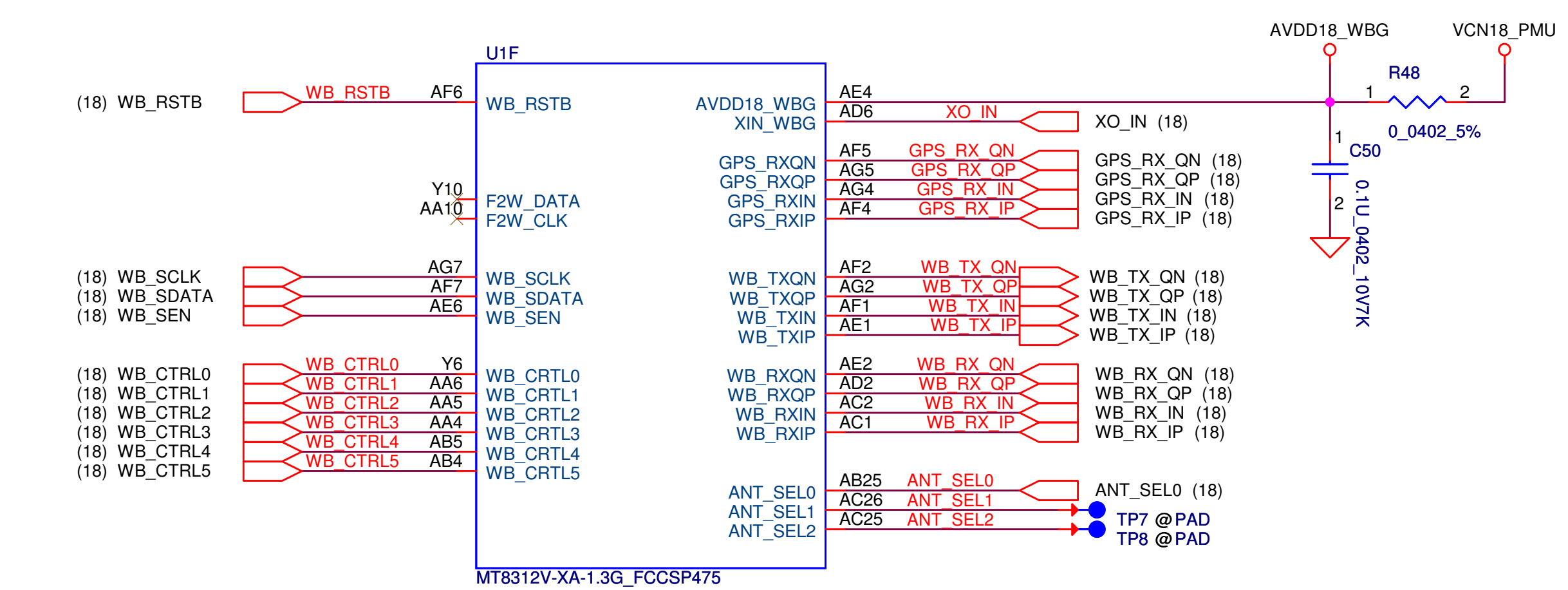
## 3G



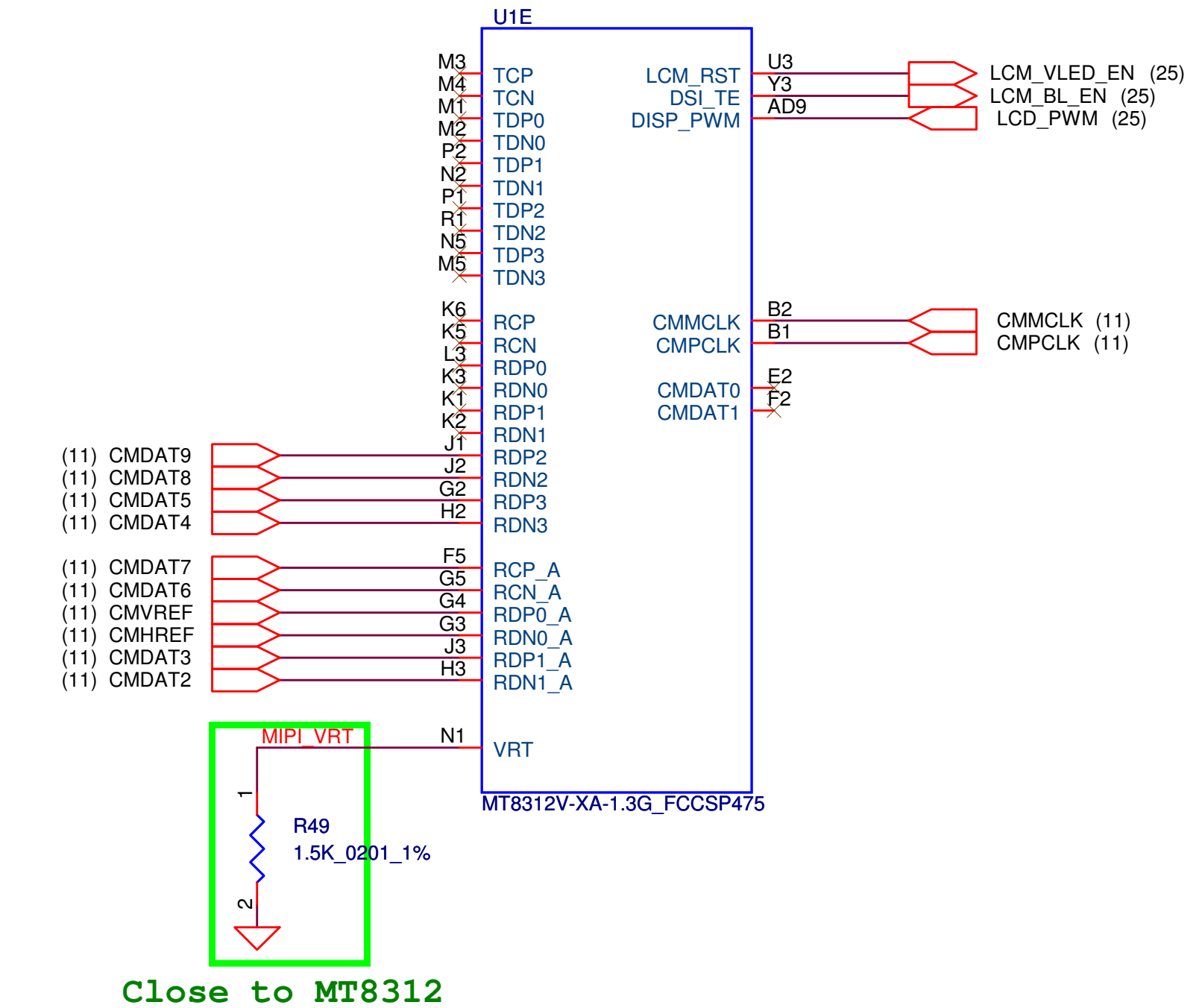
## DDR3L



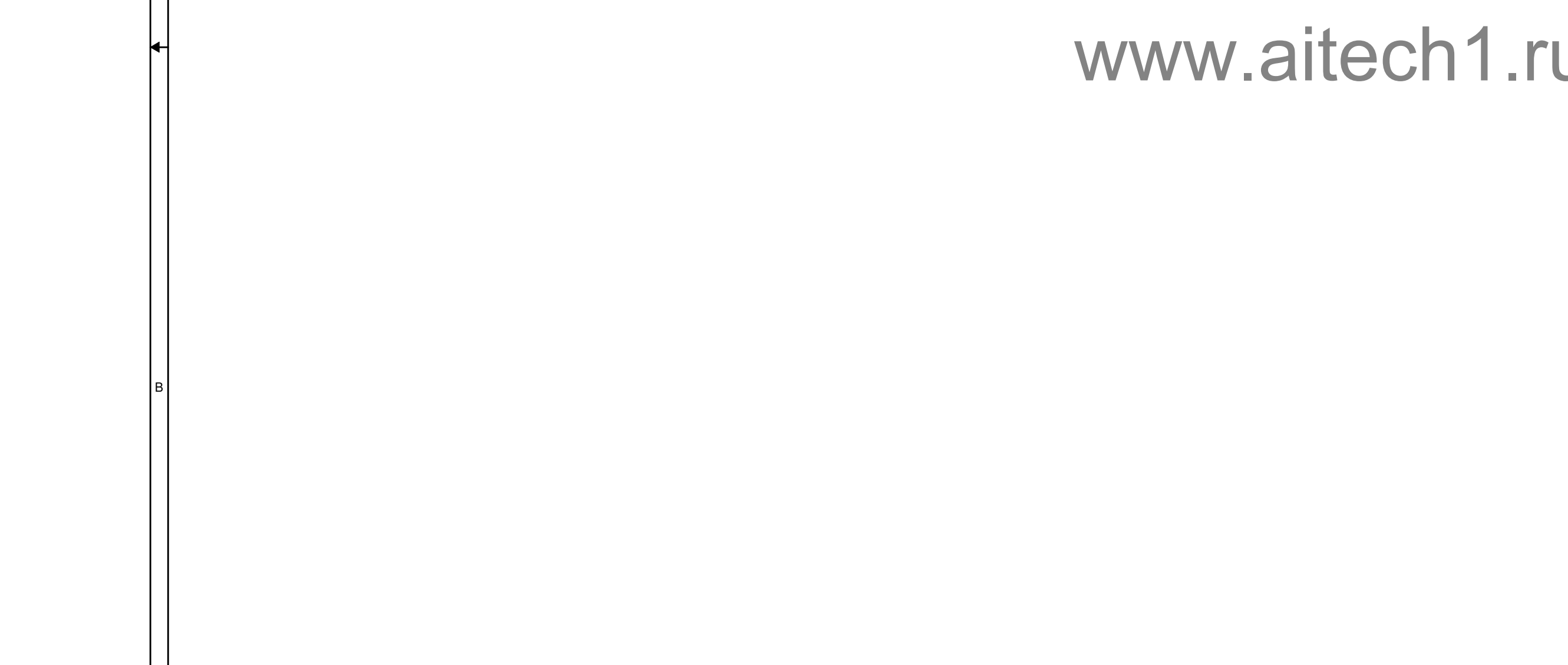
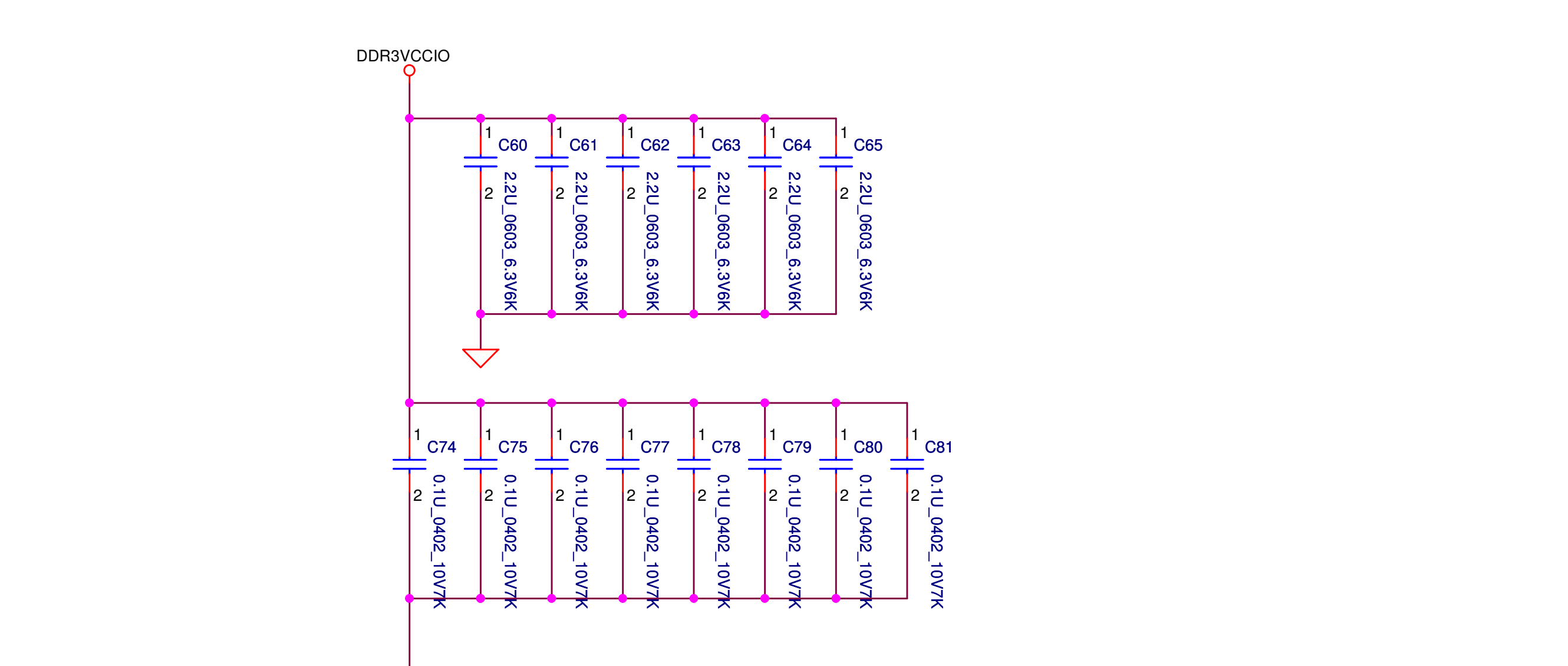
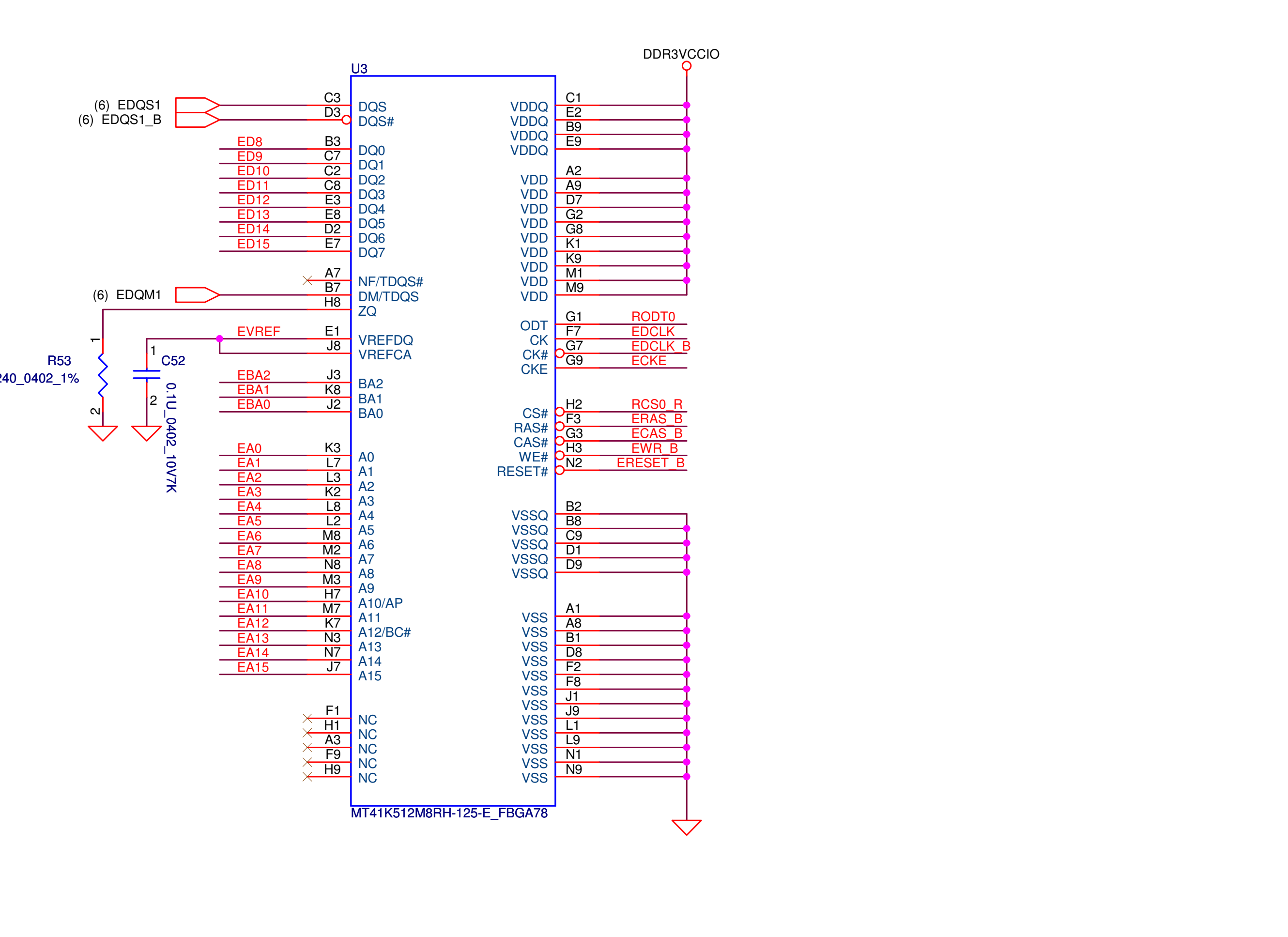
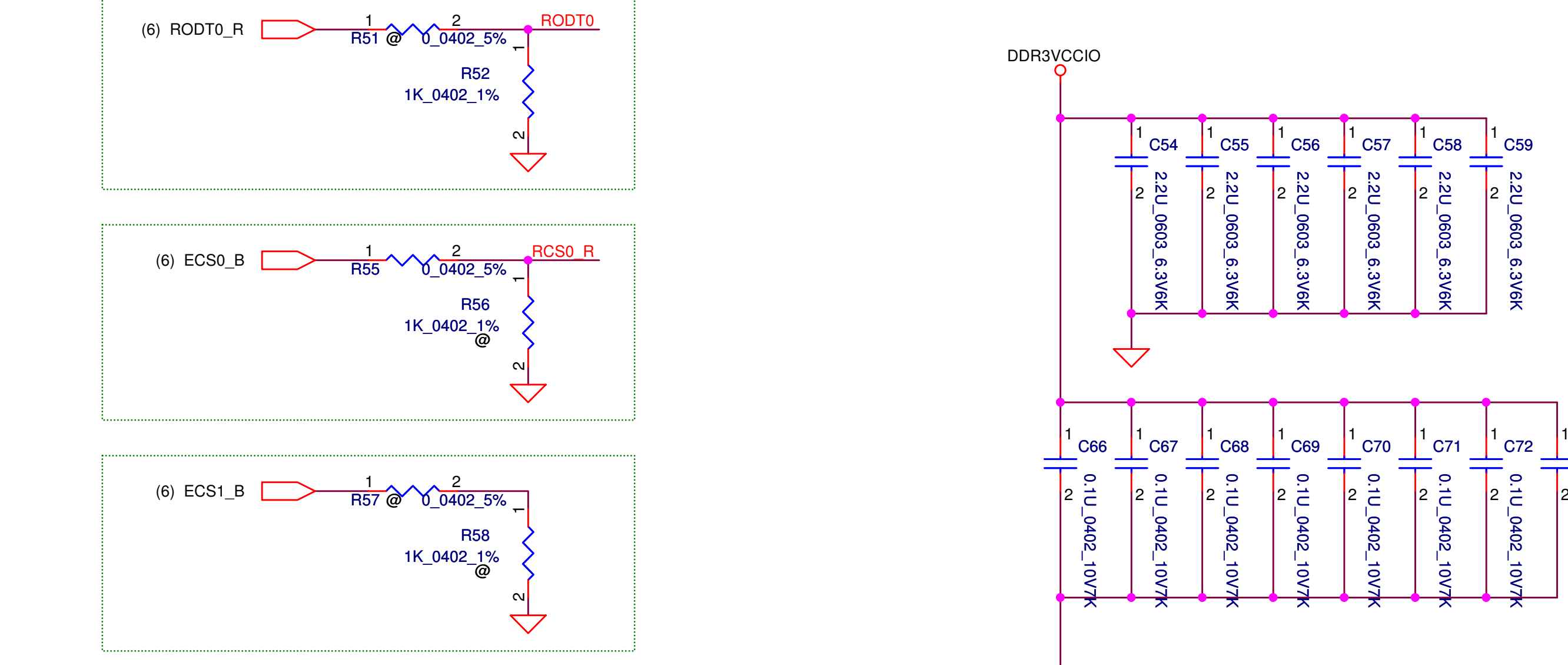
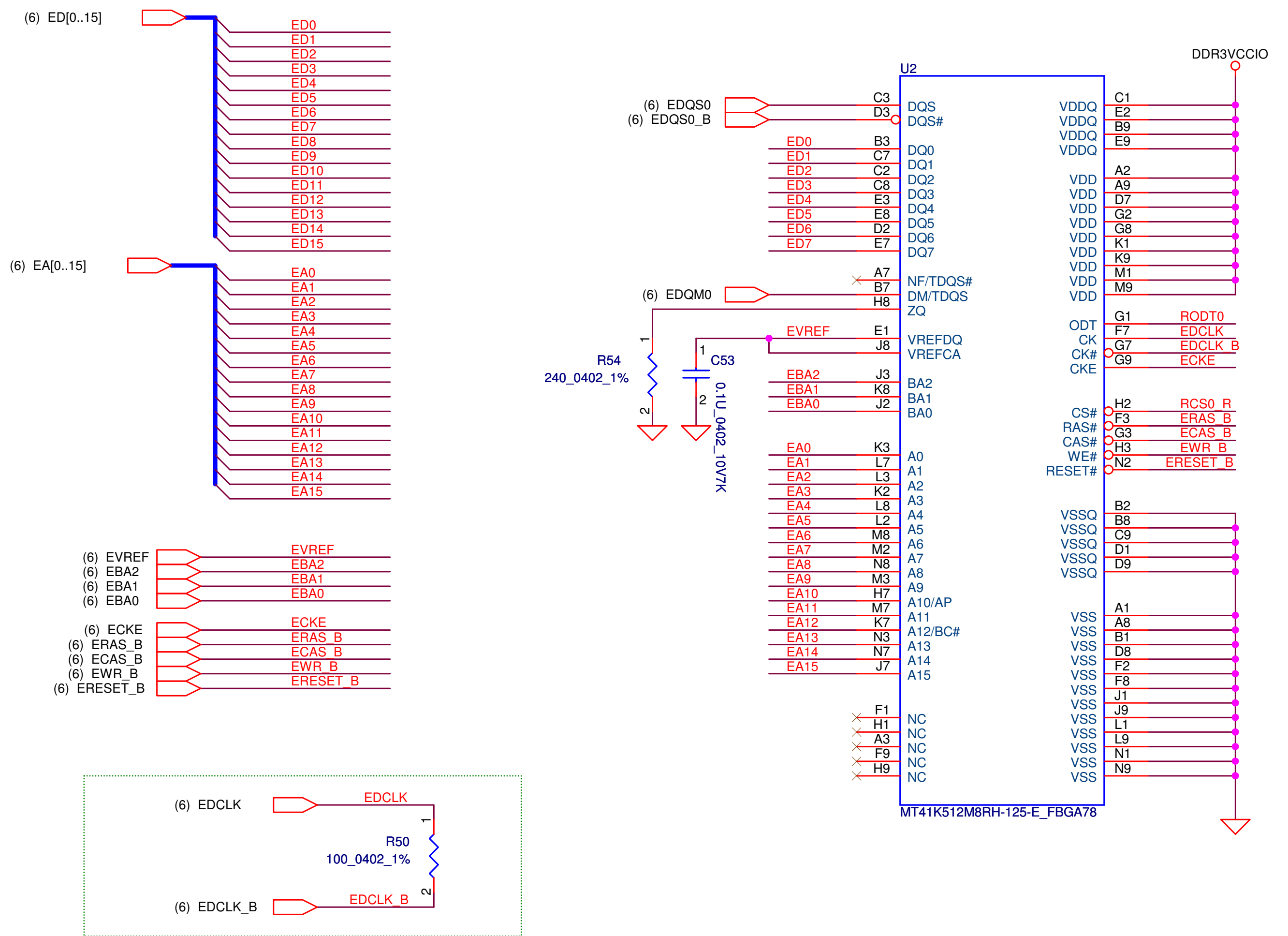
## WIFI/BT/GPS/RF



## CAMERA

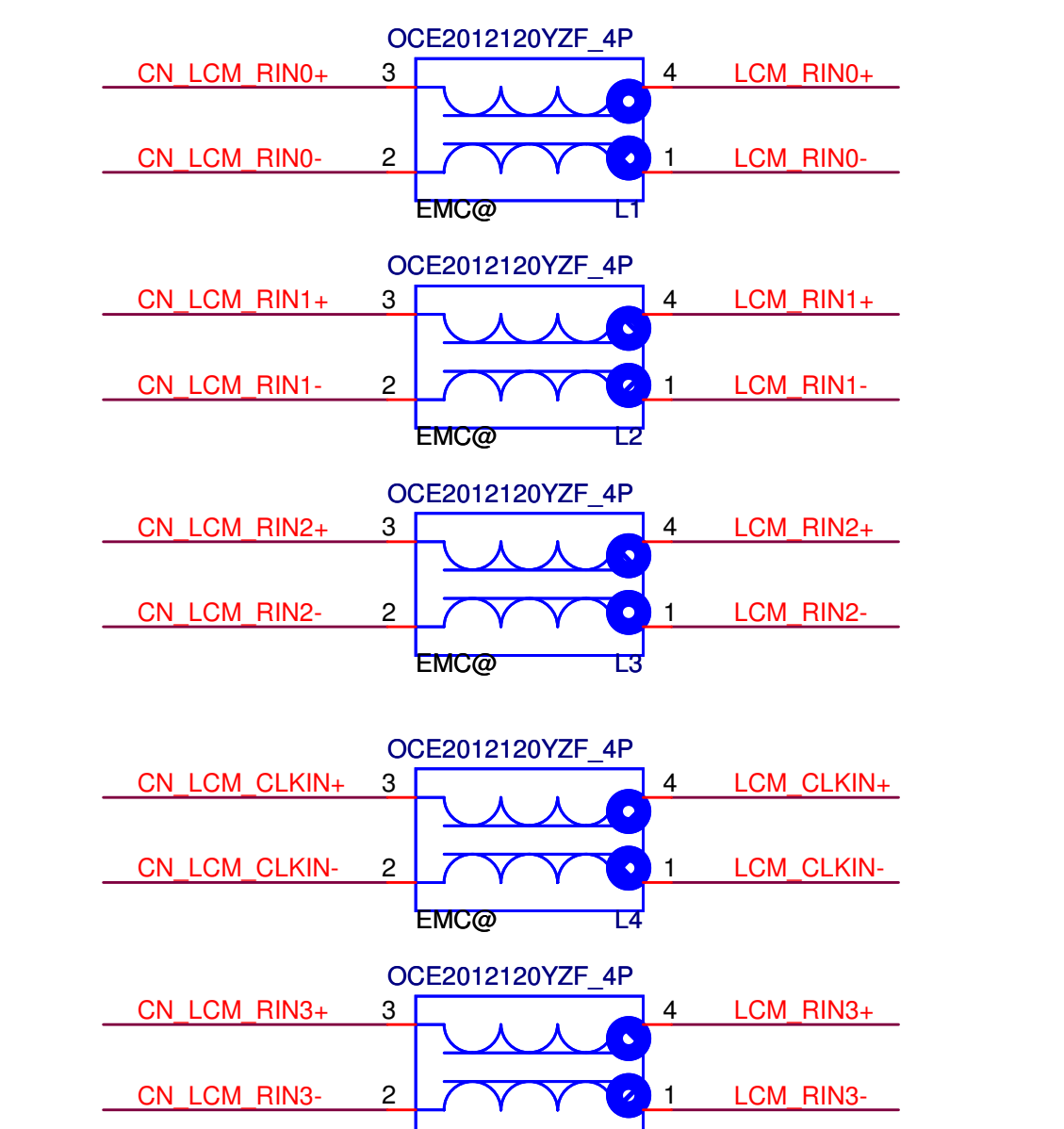
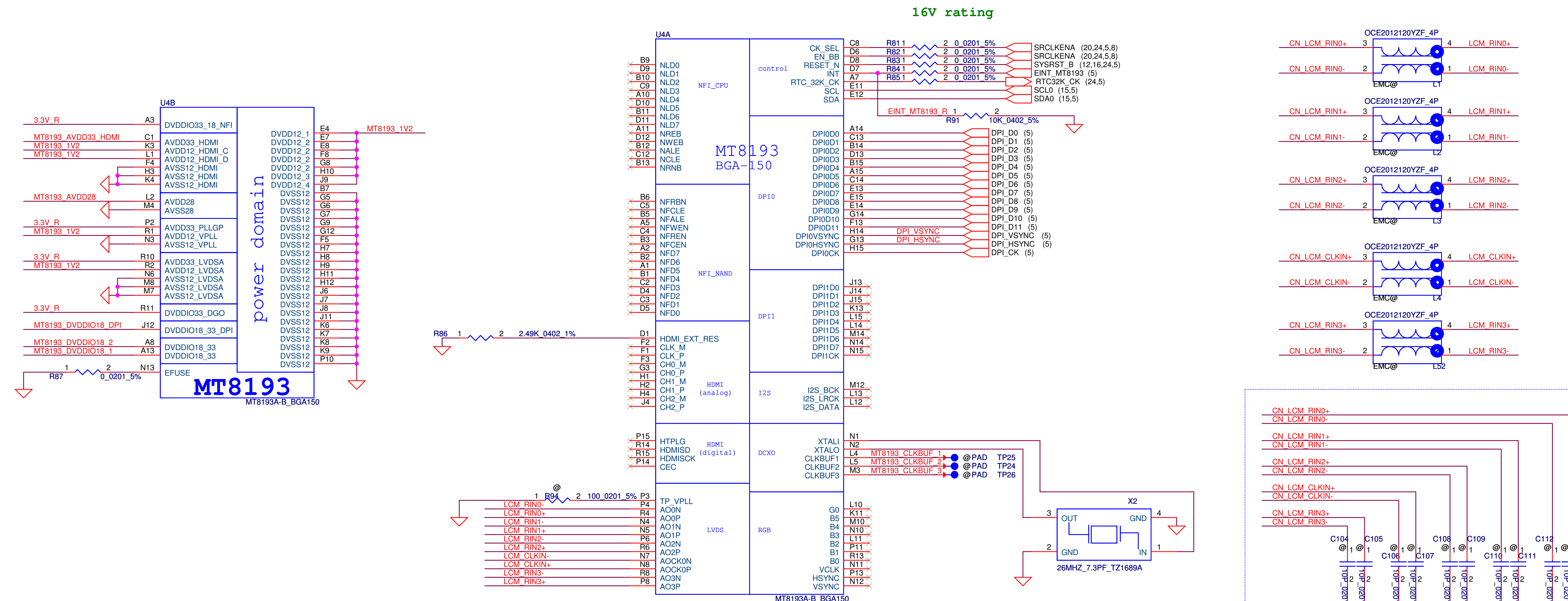
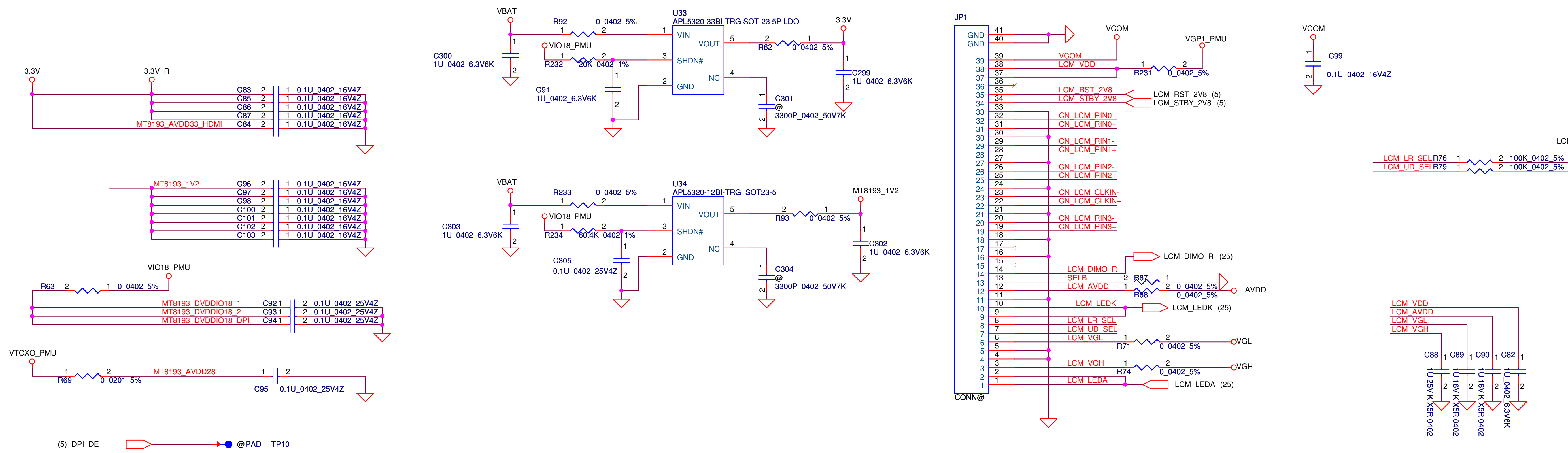


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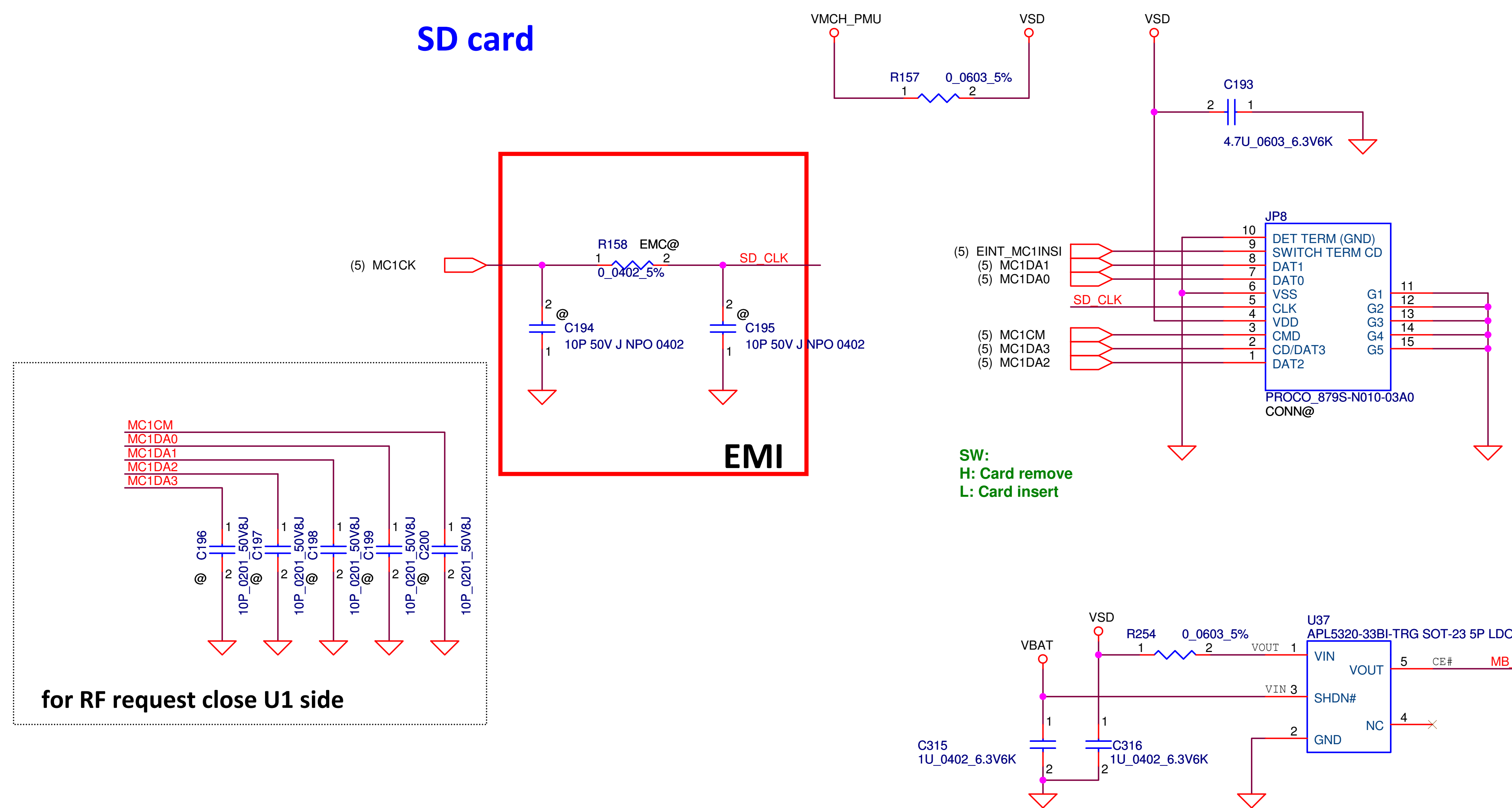
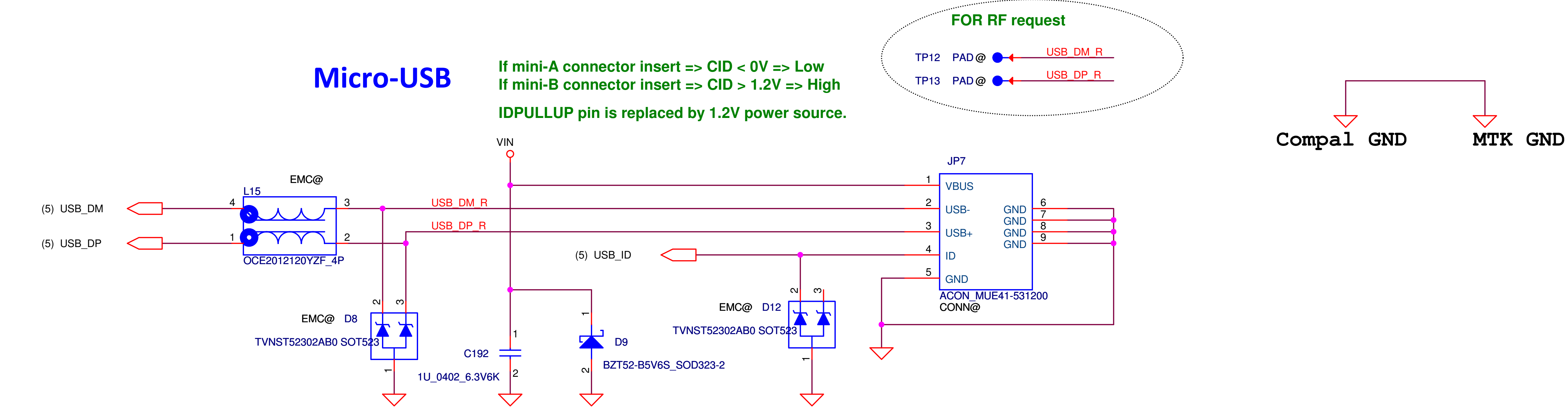




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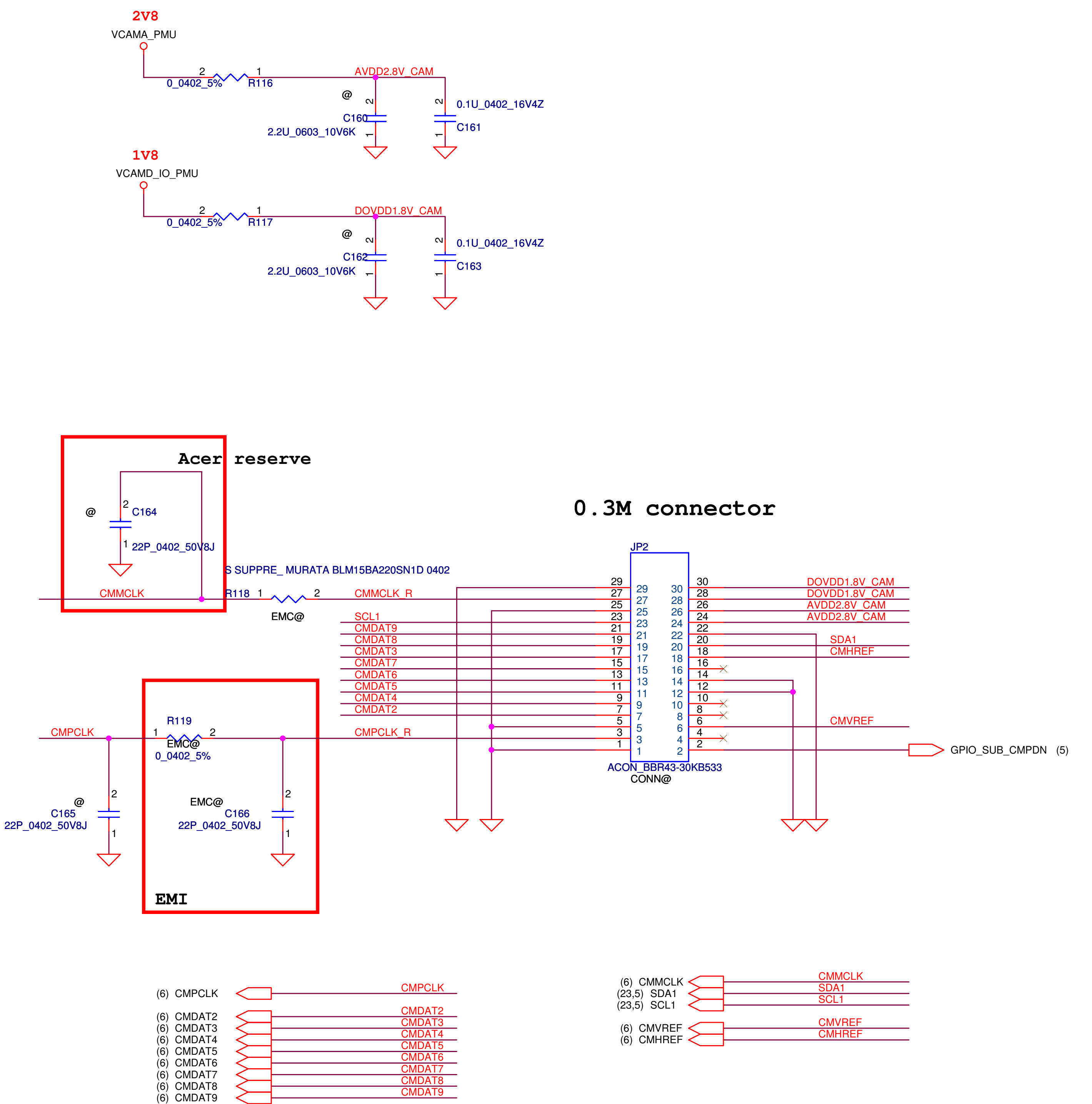








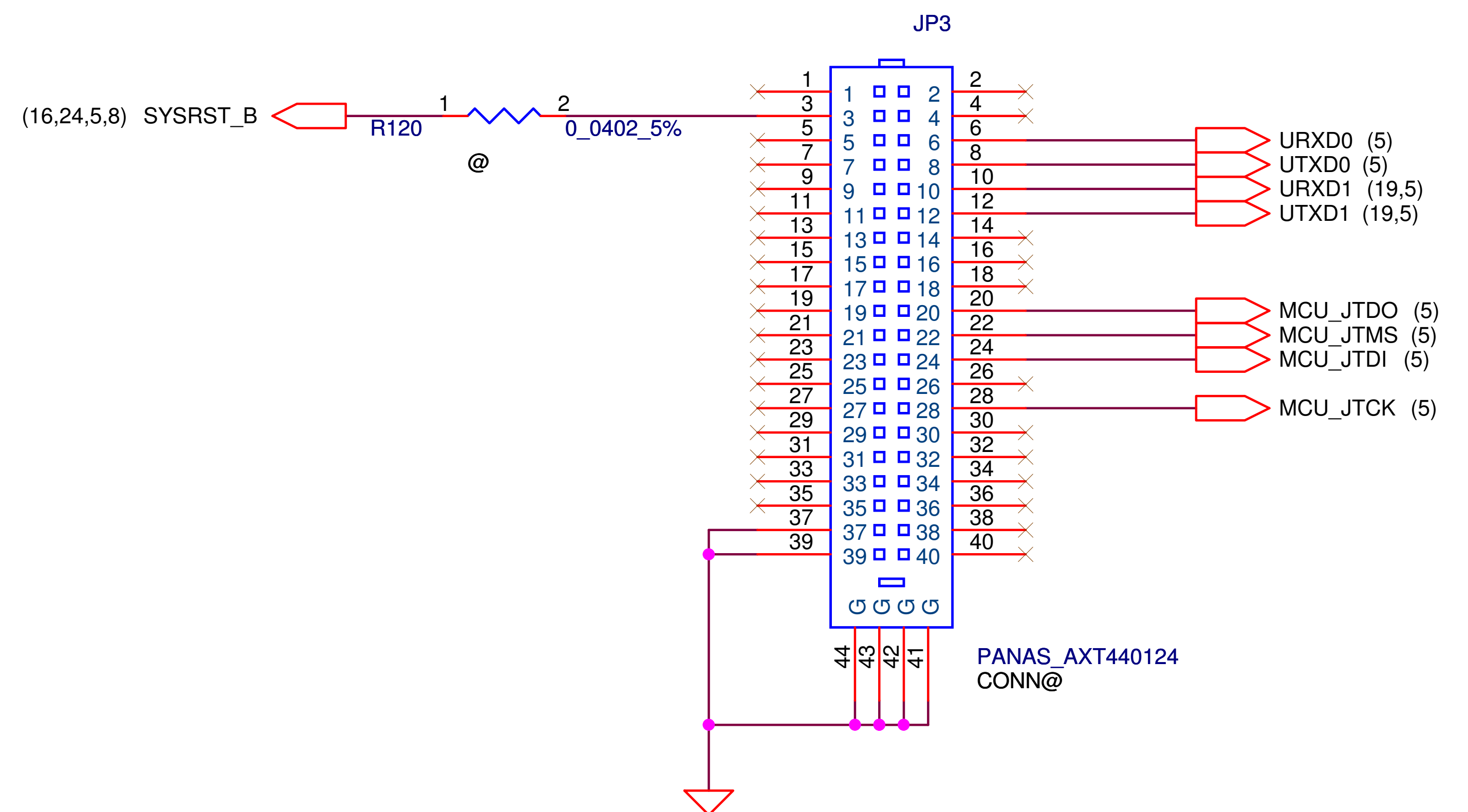
## Front Camera



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# Common Debug

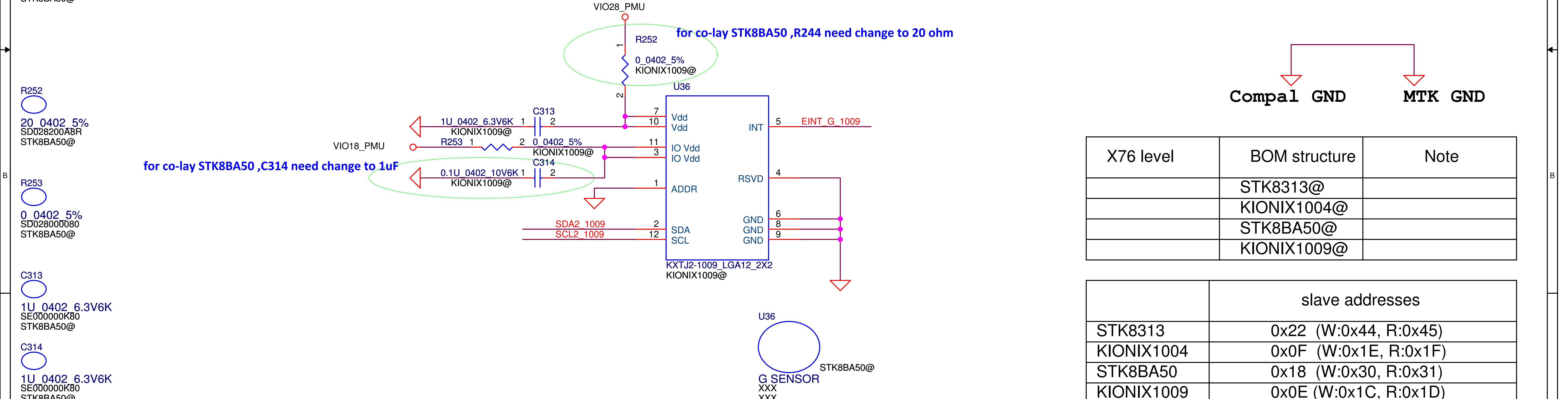
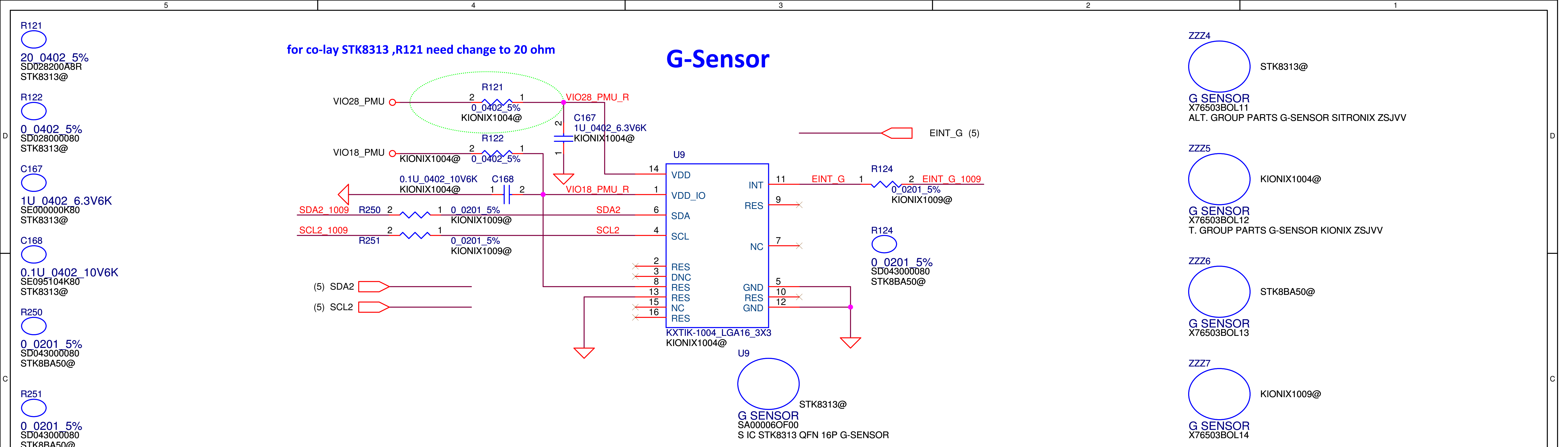


Net	Aux Func.0	Aux Func.1	Aux Func.2	Aux Func.3	Aux Func.4	Aux Func.5
JTMS	GPIO76	B1:JTMS	I1:CONN_MCU_TMS			
JTCK	GPIO77	I0:JTCK	I0:CONN_MCU_TCK1			
JTDI	GPIO78	I1:JTDI	I0:CONN_MCU_TDI			
JTDO	GPIO79	O:JTDO	O:CONN_MCU_TDO			
EINT1	GPIO1	O:PWM2	O:DPI_D5	I0:MD_EINT1	O: TDD_TDO	O:CONN_MCU_TDO
EINT2	GPIO2	O:CLKM0	O:DPI_D6	I0:MD_EINT2		O:CONN_MCU_DBGACK_N
EINT3	GPIO3	O:CLKM1	O:DPI_D7	I0:SPI_MI	I0:MD_EINT3	I1:CONN_MCU_DBGI_N
EINT4	GPIO4	O:CLKM2	O:DPI_D8	O:SPI_MO	I1:TDD_TCK	I0:CONN_MCU_TCK0
EINT5	GPIO5	I1:UCTS2	O:DPI_D9	O:SPI_CS	I1: TDD_TDI	I0:CONN_MCU_TDI
EINT6	GPIO6	O:URTS2	O:DPI_D10	O:SPI_CK	I0:TDD_TRSTN	I0:CONN_MCU_TRST_B
EINT7	GPIO7	I1:UCTS3	O:DPI_D11	B1:SDA1	I1: TDD_TMS	I1:CONN_MCU_TMS
ANT_SEL0	GPIO47	O:ANT_SEL0	O:PWM0	O:CONN_MCU_DBGACK_N		
ANT_SEL1	GPIO48	O:ANT_SEL1	O:PWM1	I1:CONN_MCU_DBGI_N		
ANT_SEL2	GPIO49	O:ANT_SEL2	O:PWM2	I0:CONN_MCU_TRST_B		

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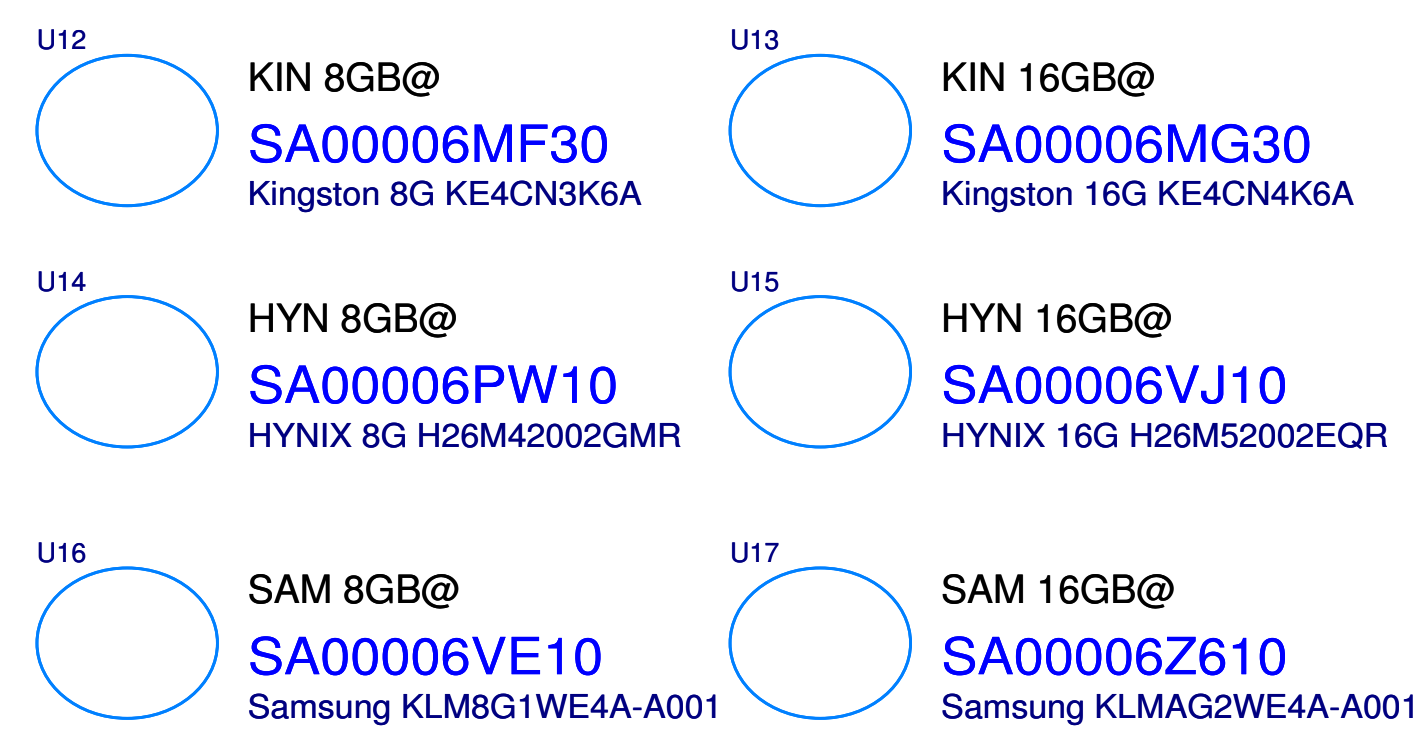
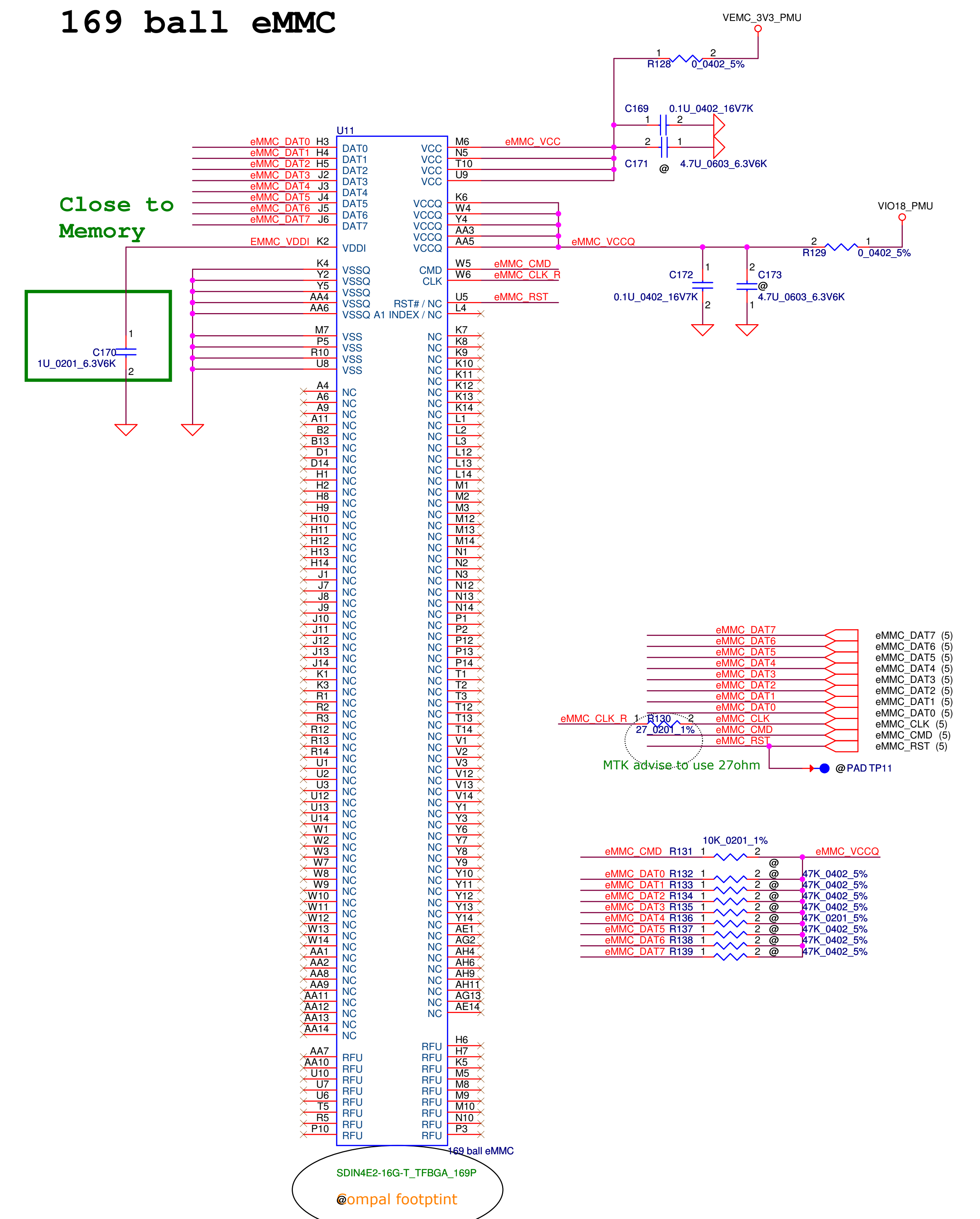
X76 level	BOM structure	Note
	STK8313@	
	KIONIX1004@	
	STK8BA50@	
	KIONIX1009@	

	slave addresses
STK8313	0x22 (W:0x44, R:0x45)
KIONIX1004	0x0F (W:0x1E, R:0x1F)
STK8BA50	0x18 (W:0x30, R:0x31)
KIONIX1009	0x0E (W:0x1C, R:0x1D)

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## 169 ball eMMC

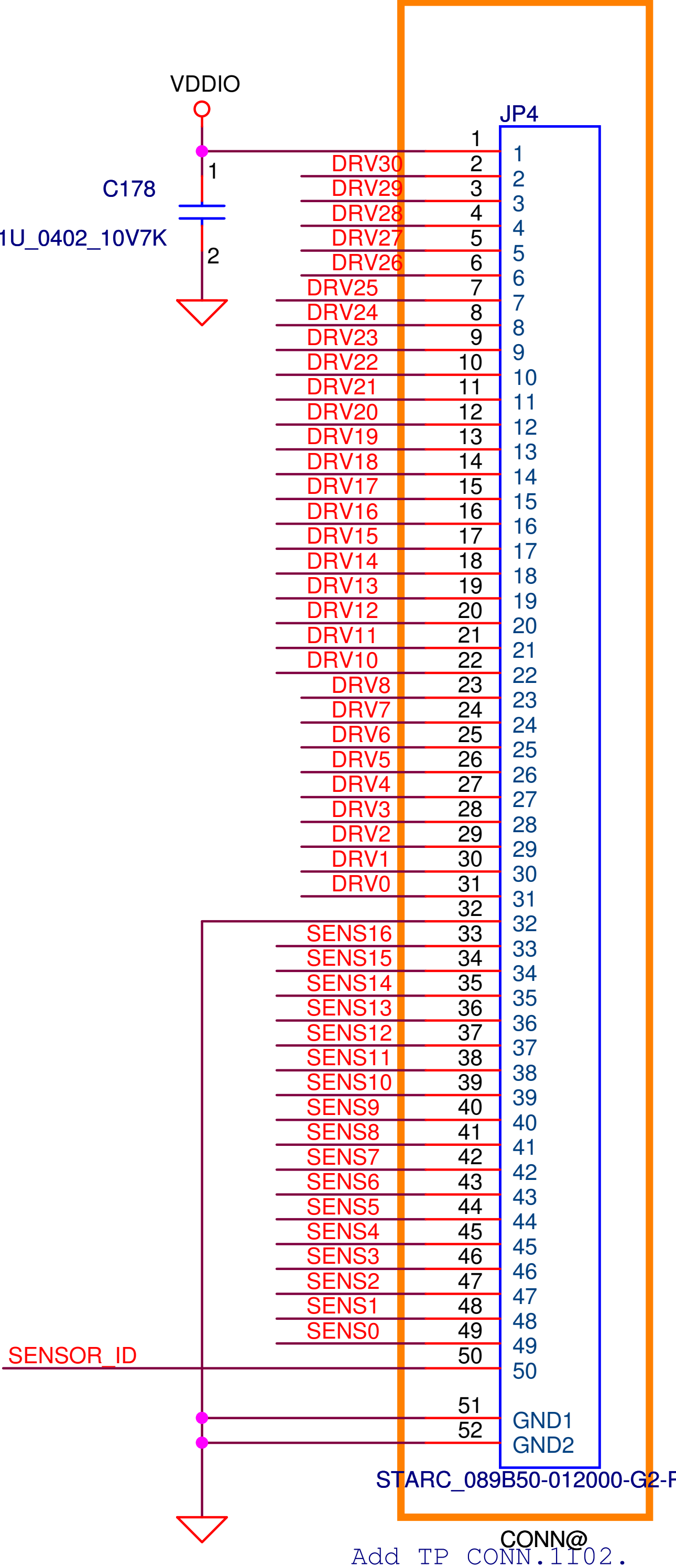
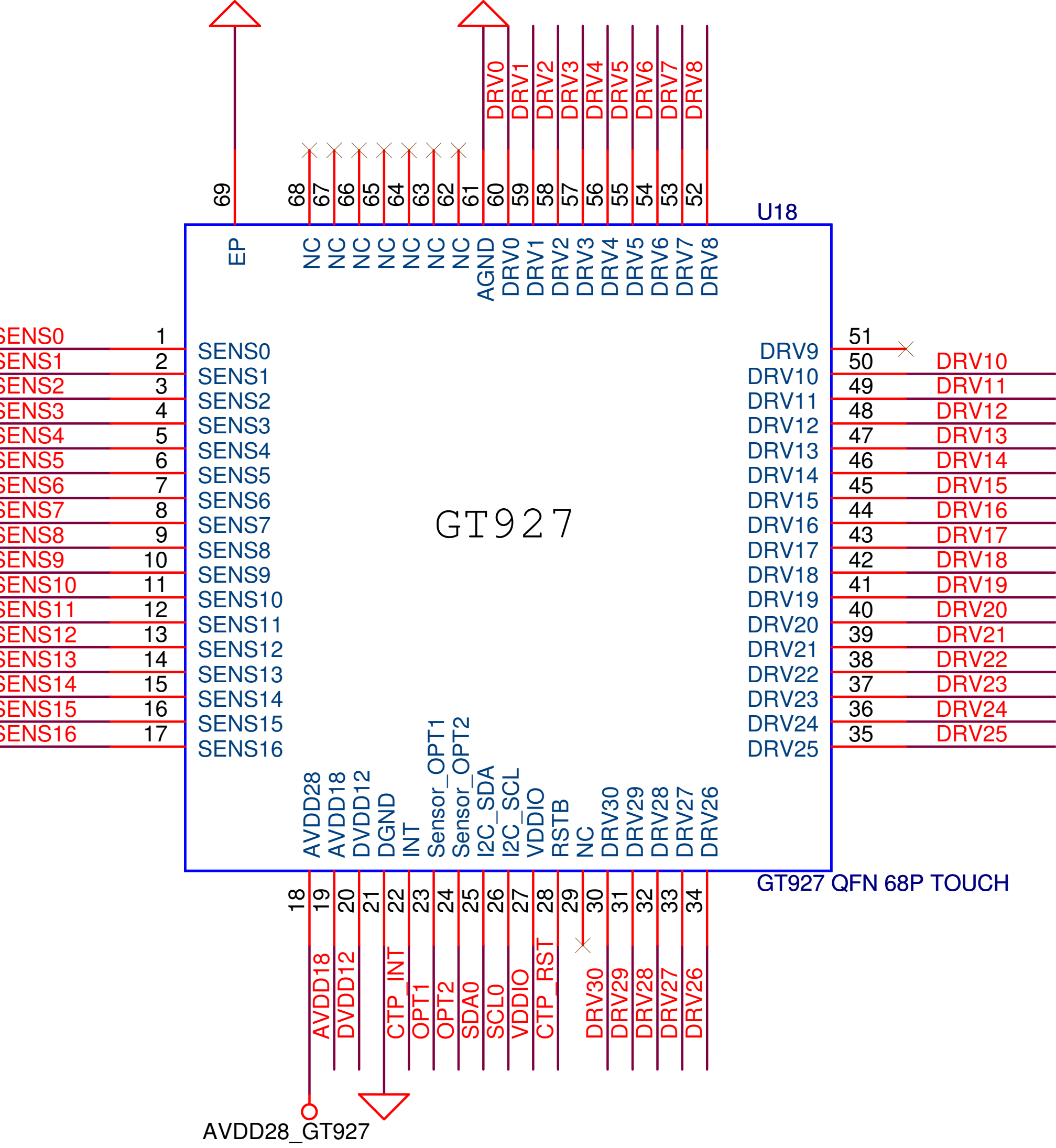
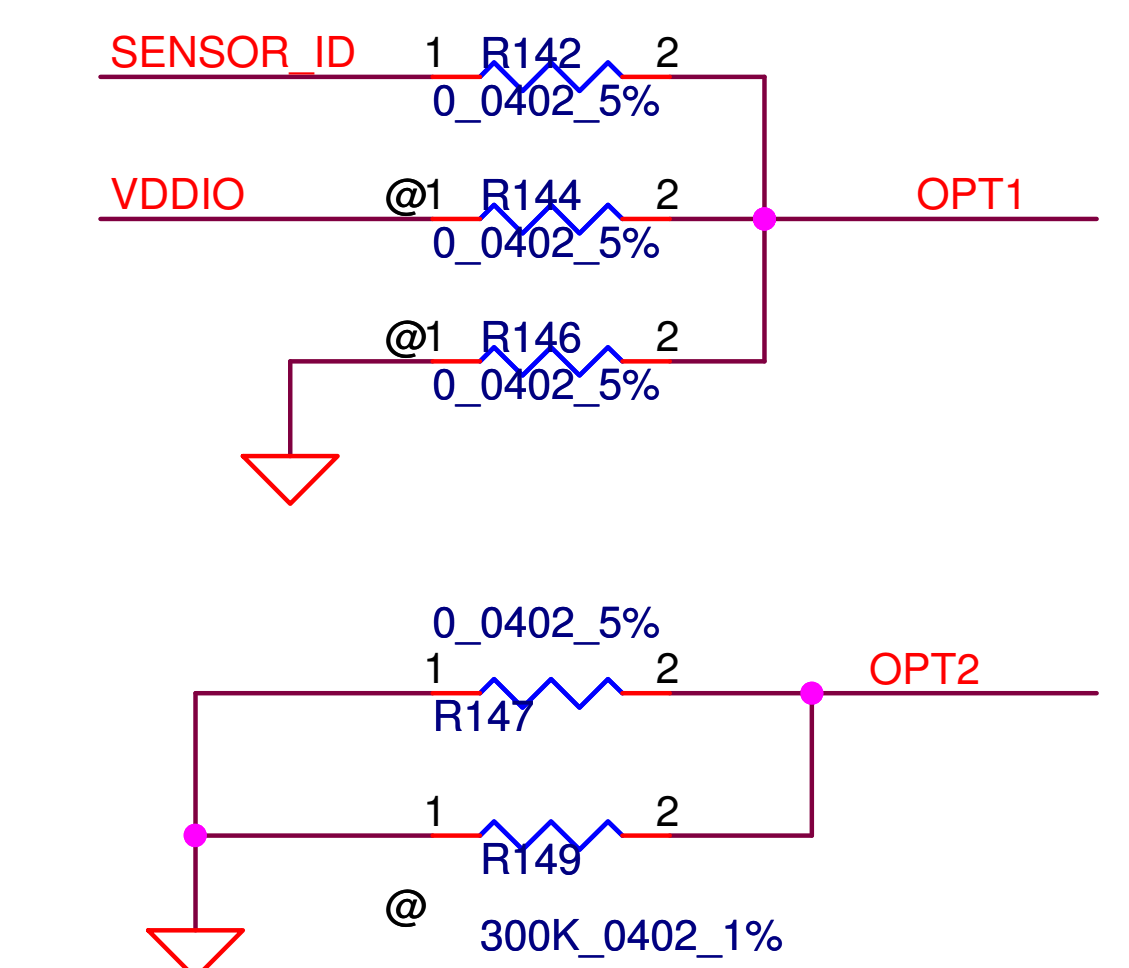
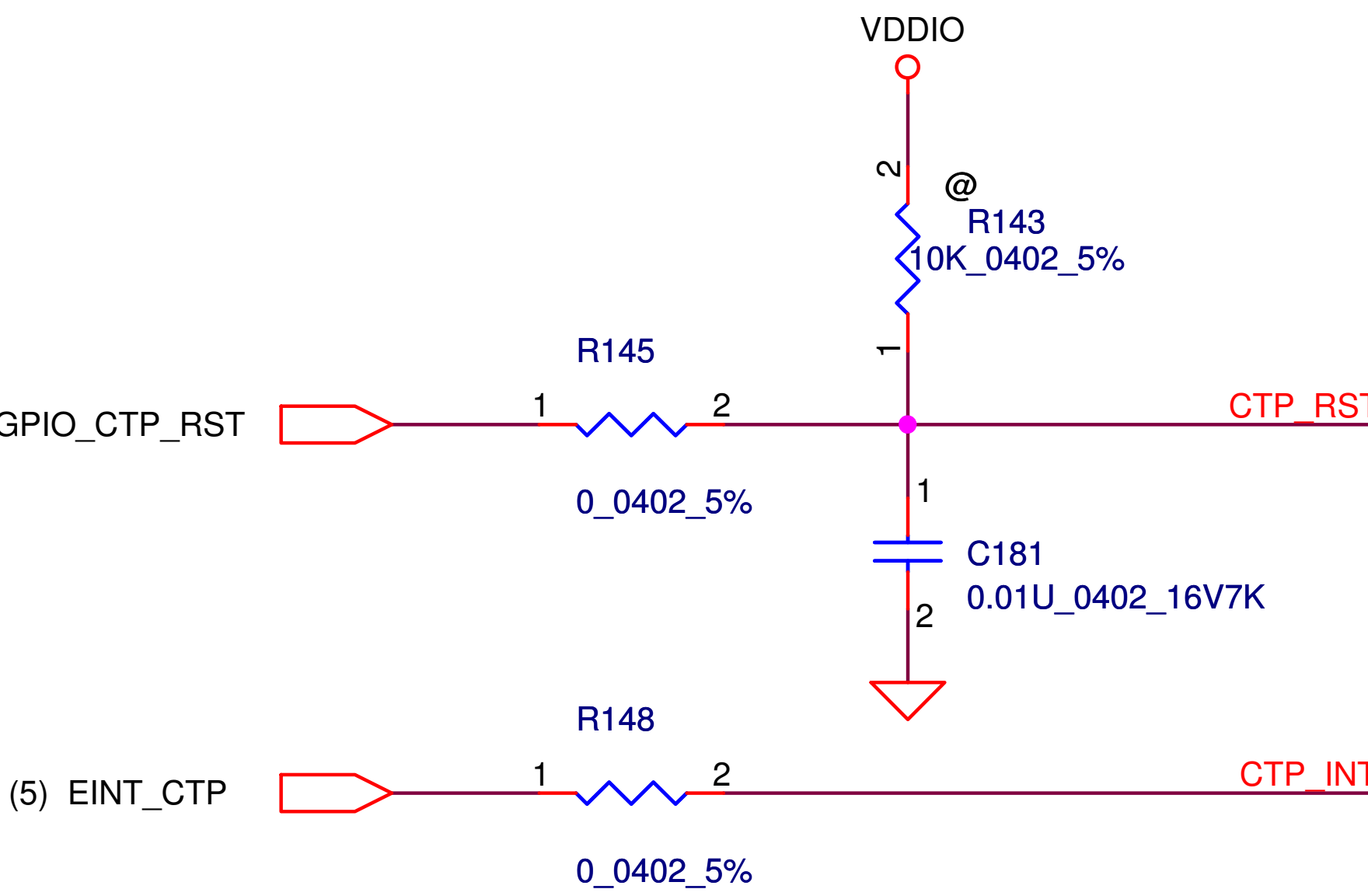
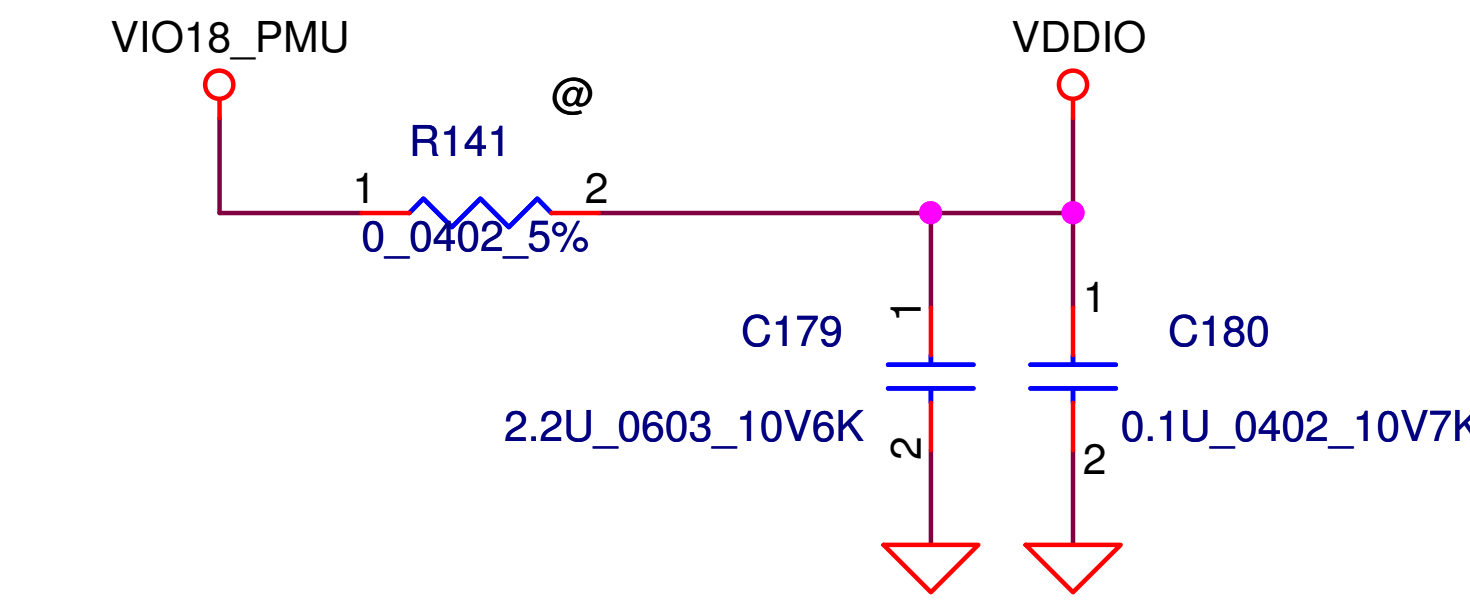
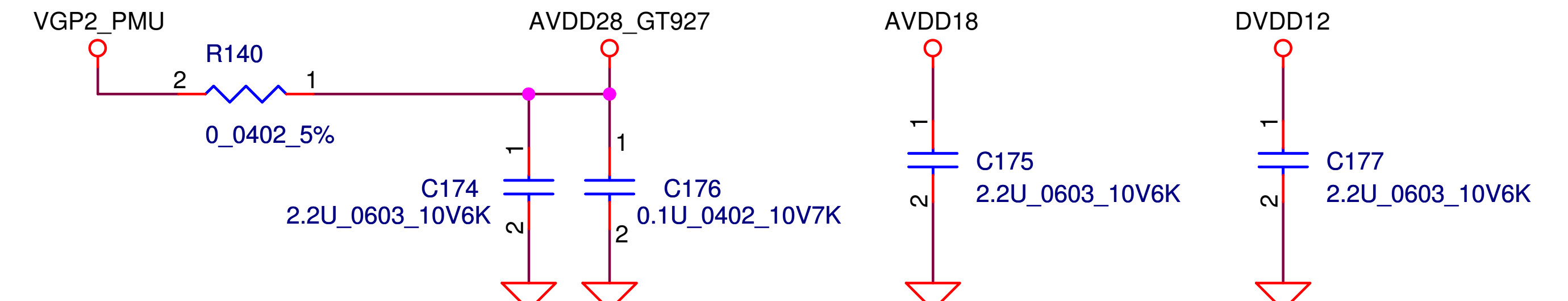


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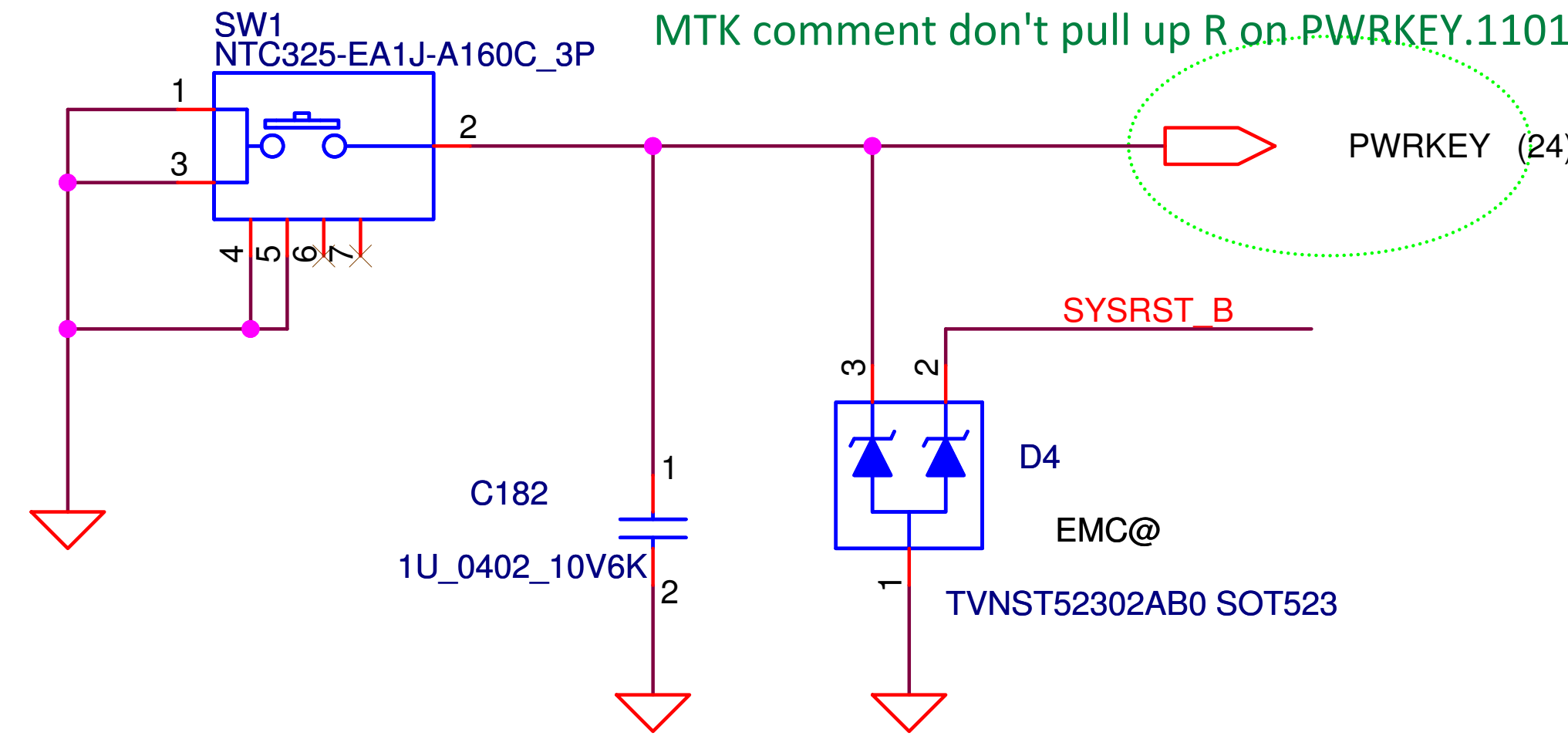


VGP2\_PMU for CTP 2.8V

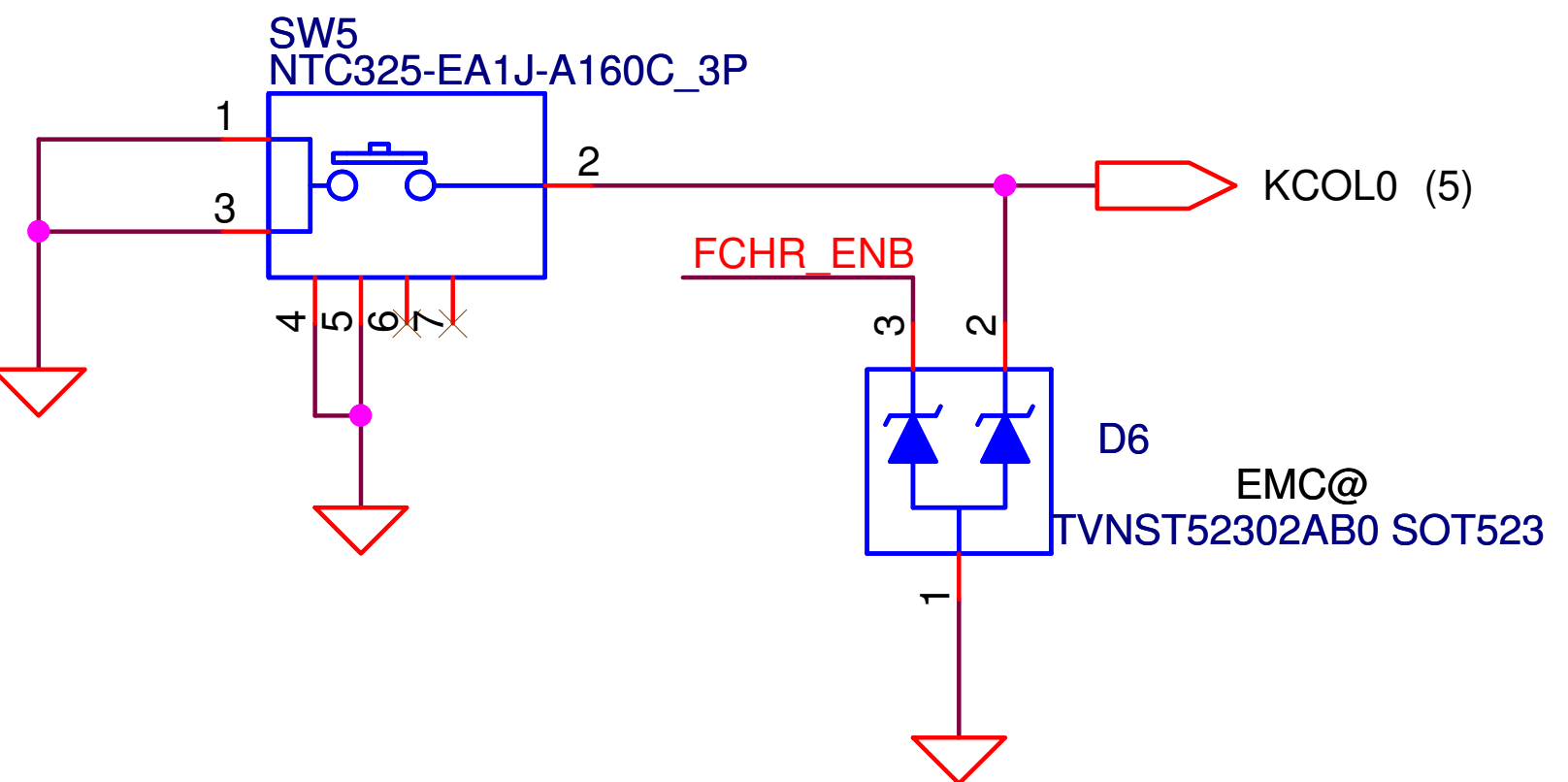


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Power-on Button



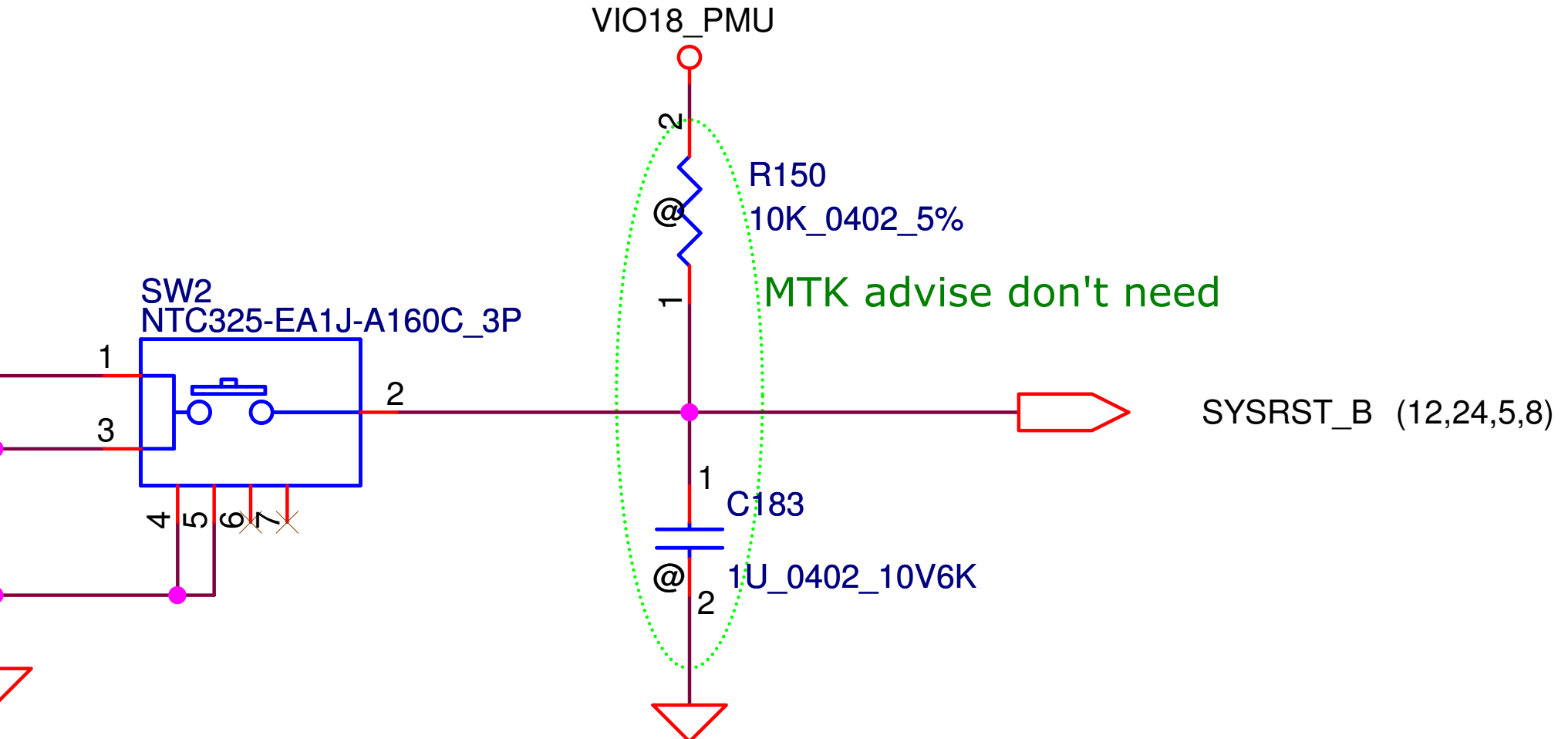
Volume Up



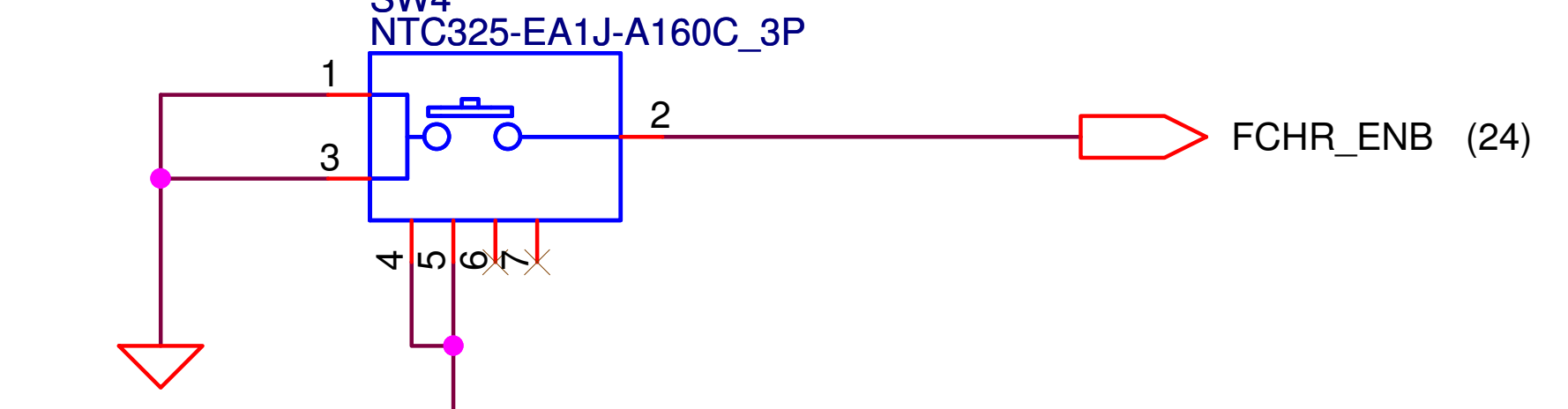
Notice :  
1. Due to KCOL0 & KROW0 reset mode = GPIO input mode, "Force USB download mode" will be fail in KCOL0+KROW0. So we change VolumeUp key=KCOL0+GND  
2. Keypad matrix will become as  
( KEY1=KCOL0+GND)

	KCOL0	KCOL1	KCOL2
KROW0	KEY 1	KEY 2	KEY 5
KROW1		KEY 3	KEY 6
KROW2		KEY 4	KEY 7

RESET



Volume Down(Download Key)



FCHR\_ENB (VOL\_DOWN#) can be configured as a normal key function  
Notice :  
There are 3 options for "Long press to shutdown" function  
1.PWRKEY + FCHR\_ENB  
2.PWERKEY only  
3.FCHR\_ENB only  
During download mode, defalut = PWRKEY + FCHR\_ENB  
For other case (exclude download mode), defalut = PWERKEY only

Compal GND MTK GND

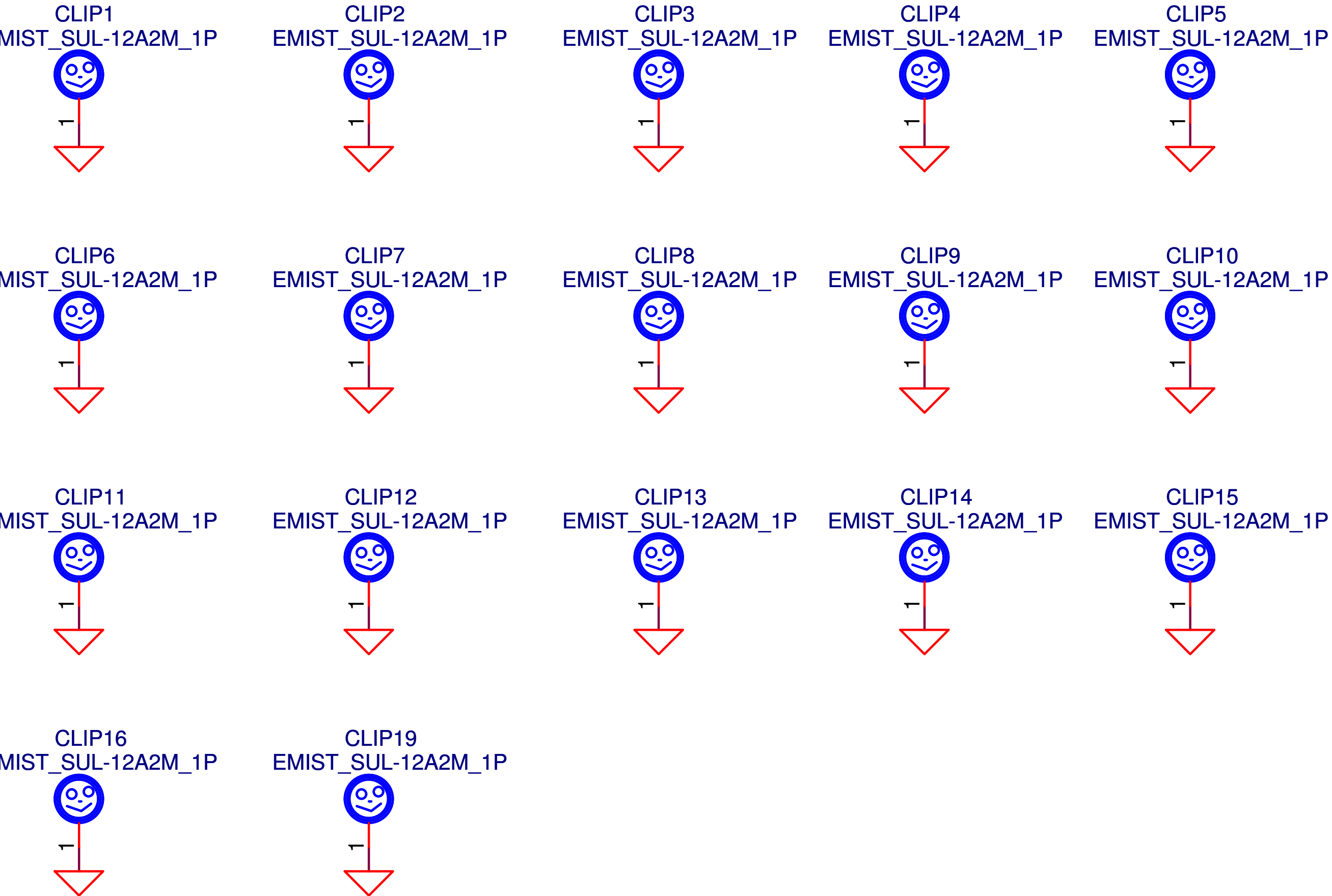
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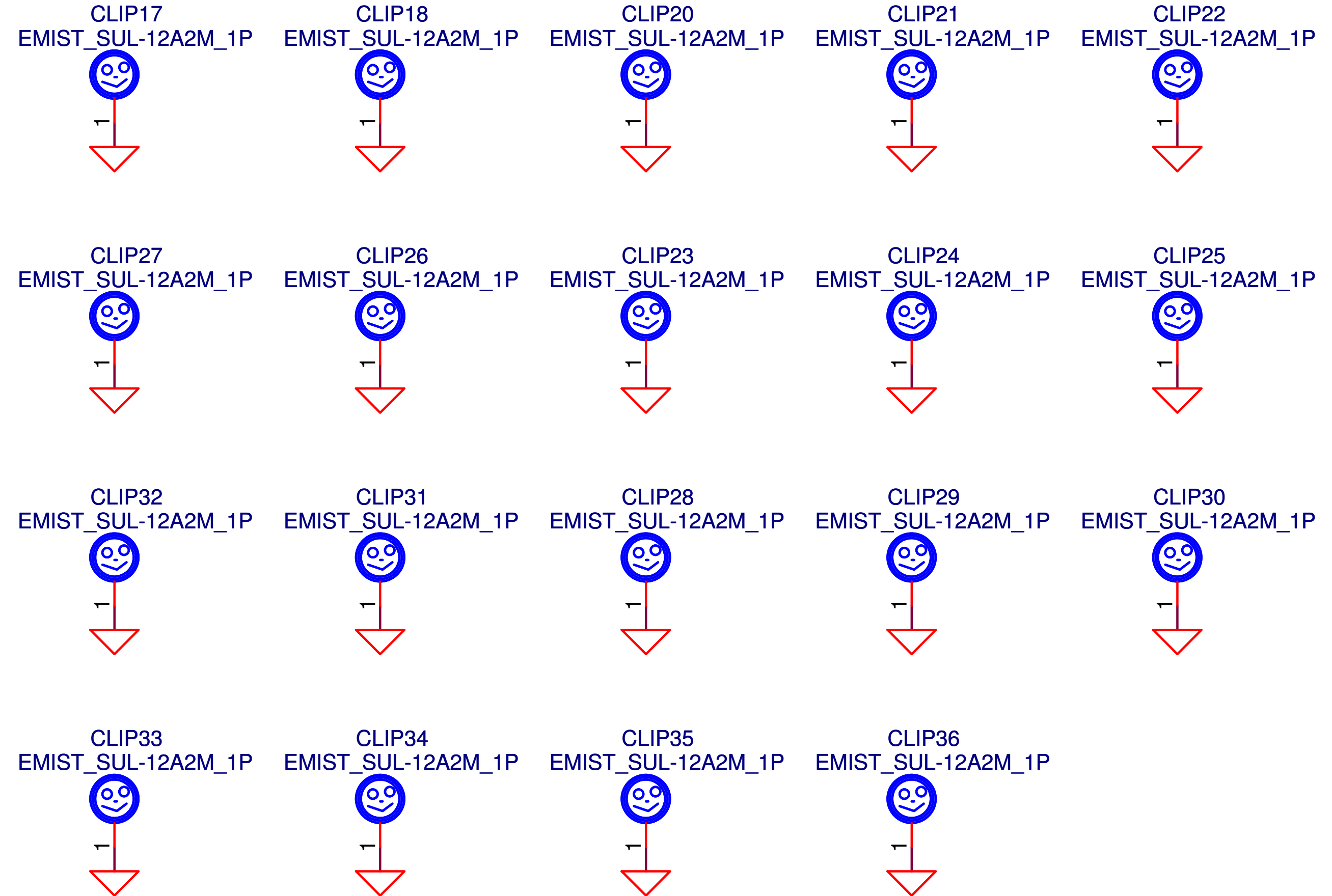


CLIP

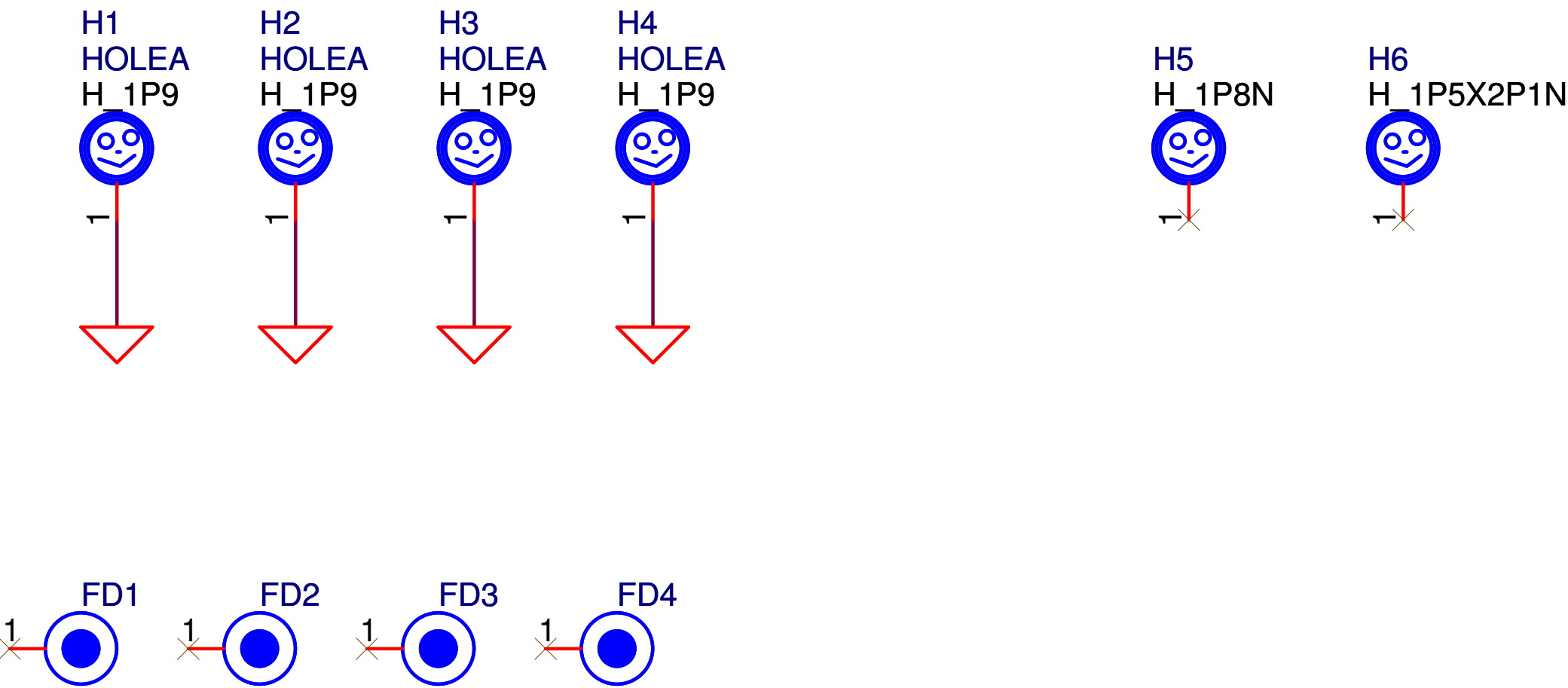
BOT



TOP

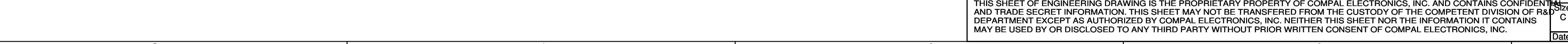


SCREW HOLE

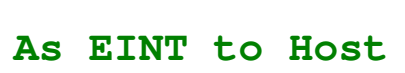
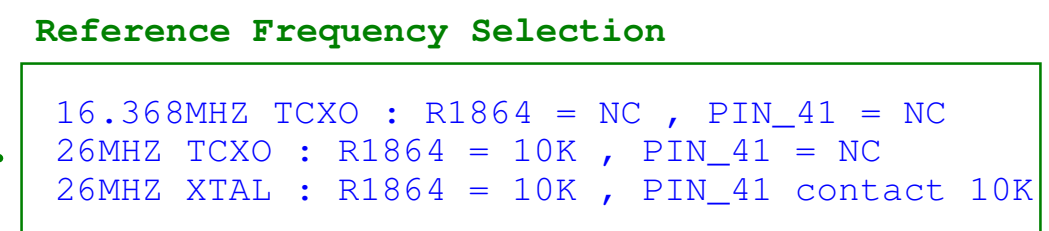
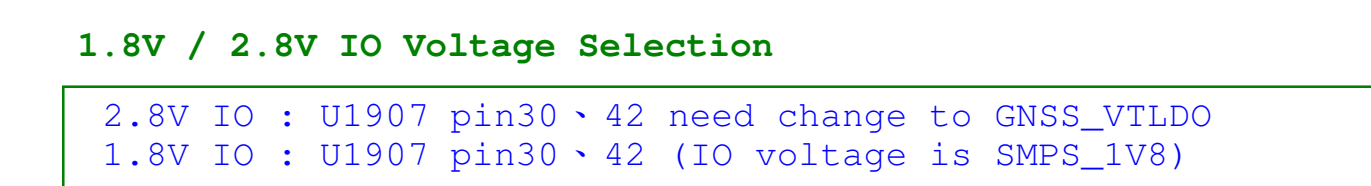
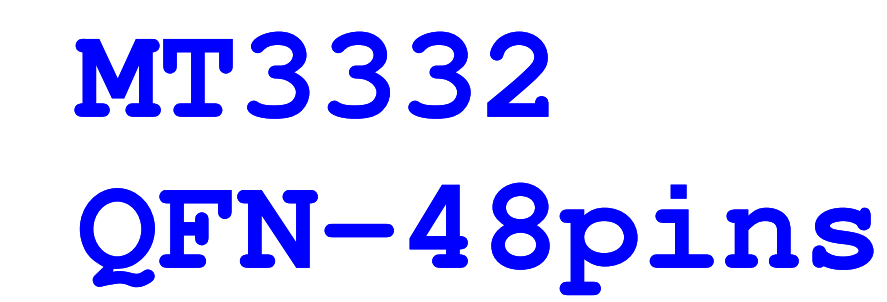
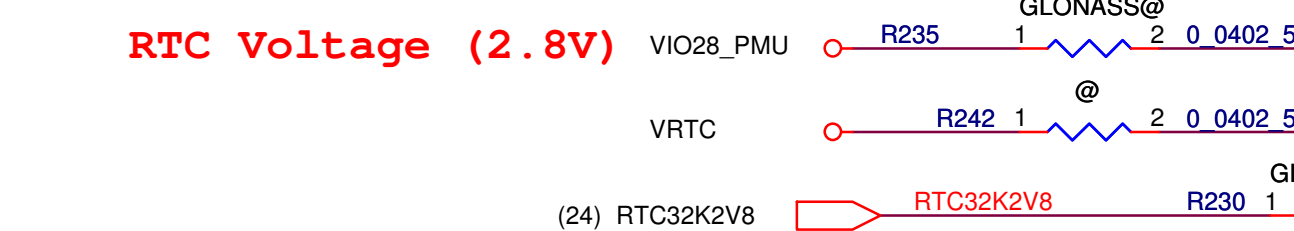
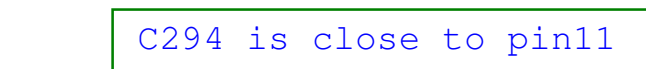
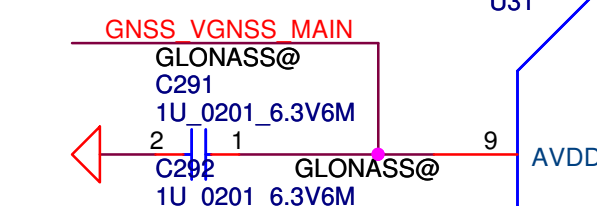
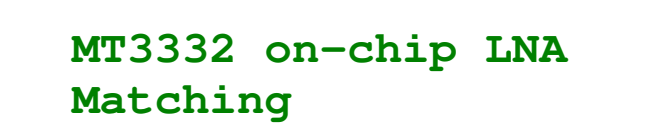


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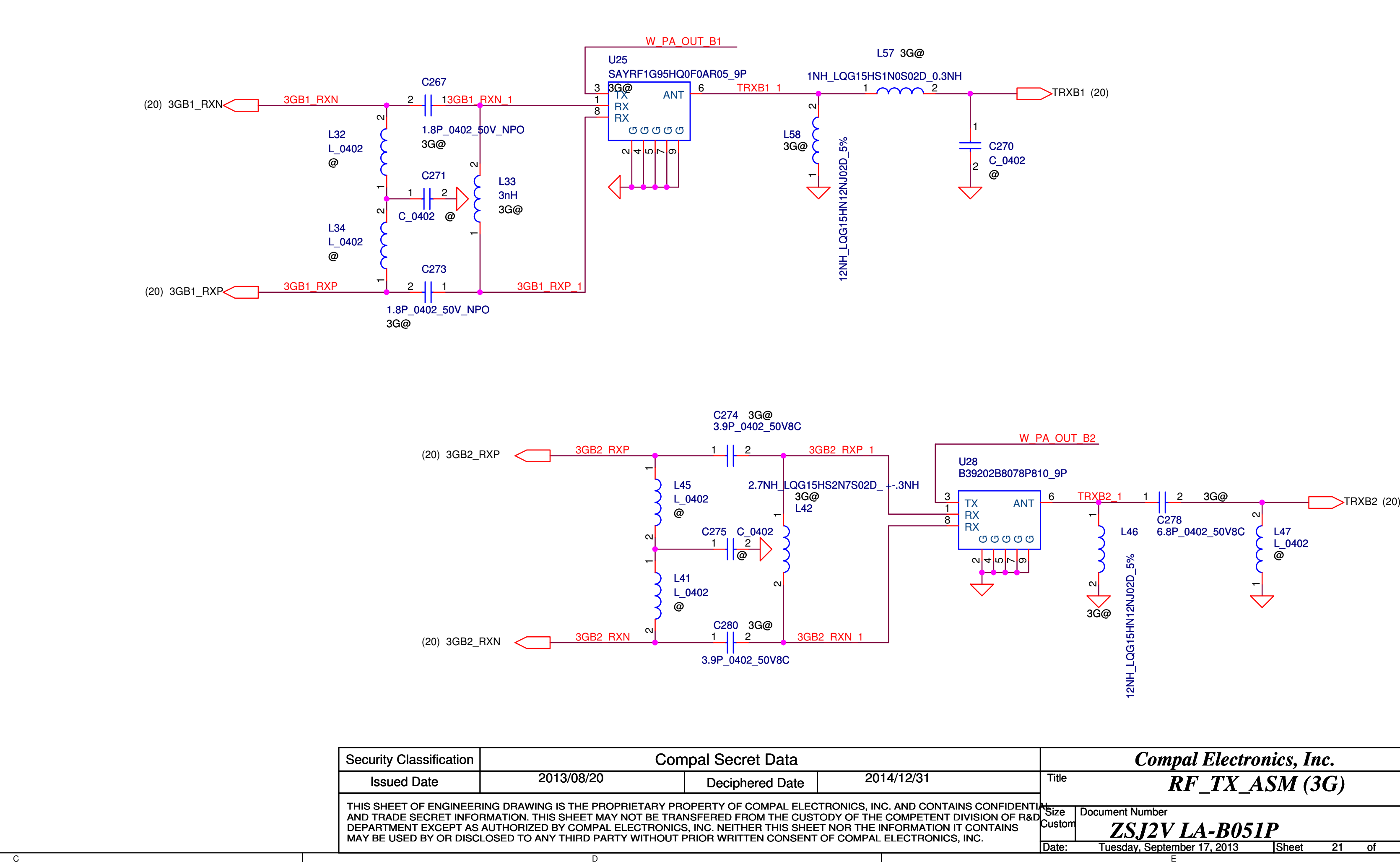
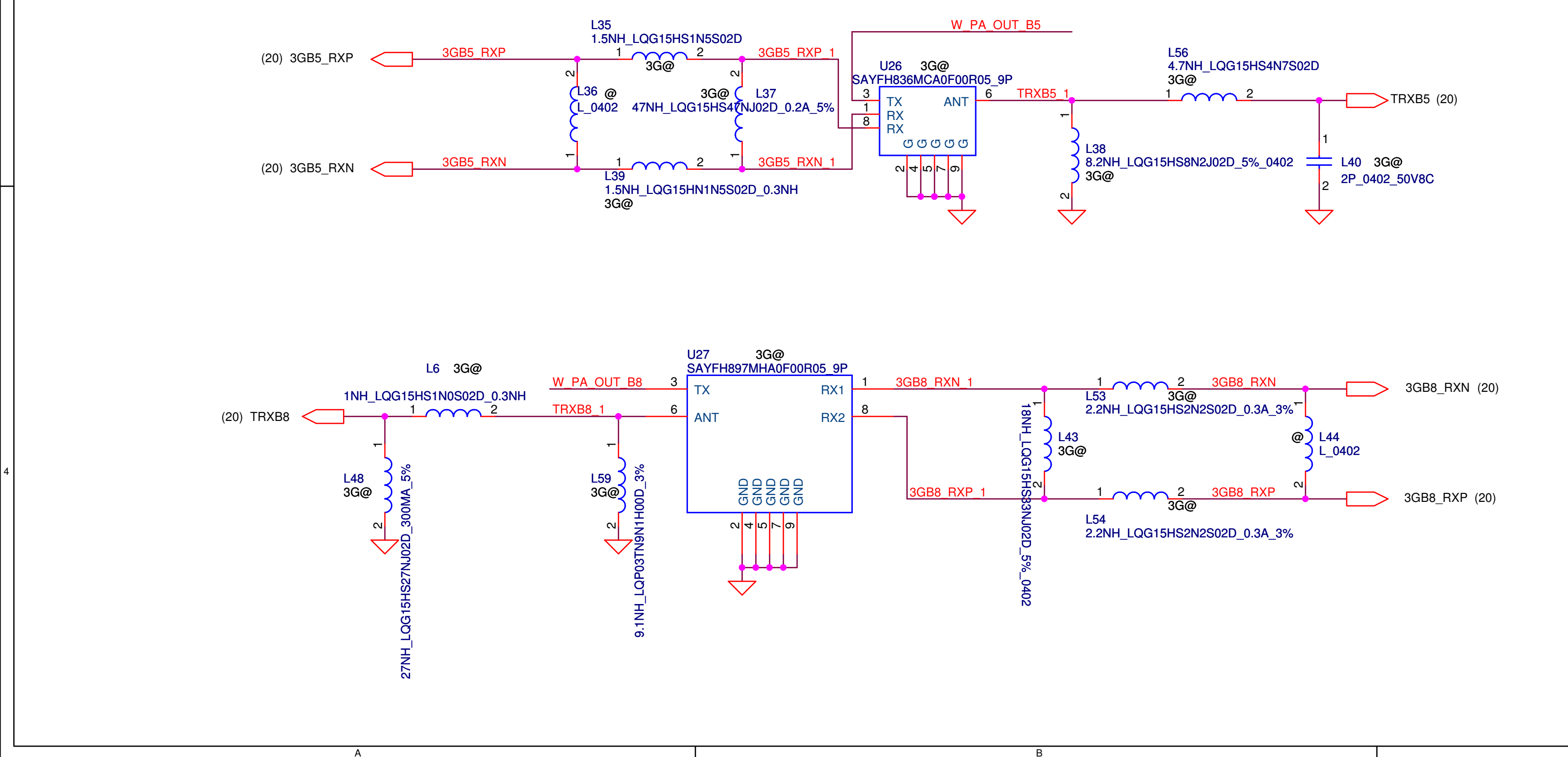


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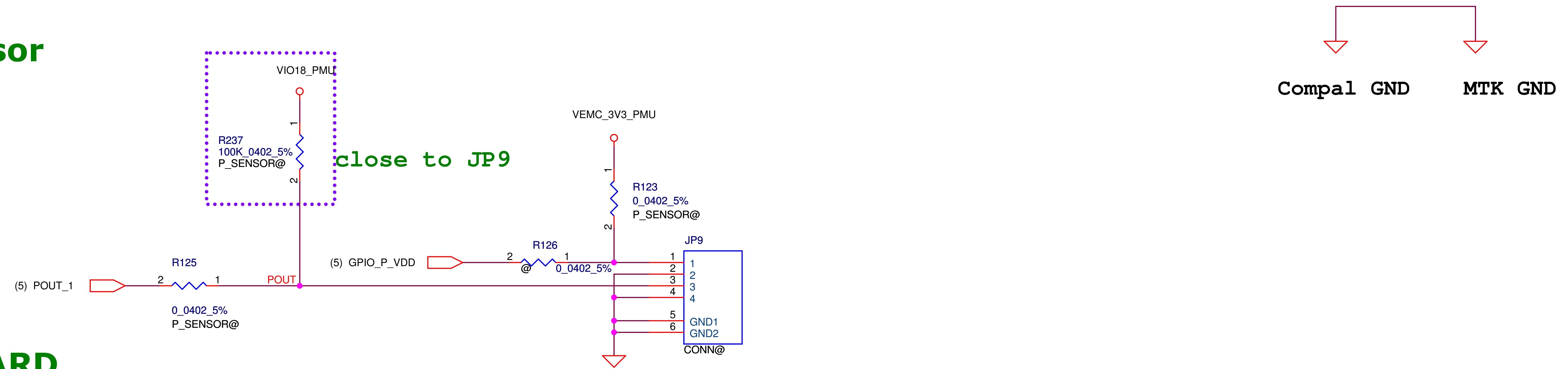




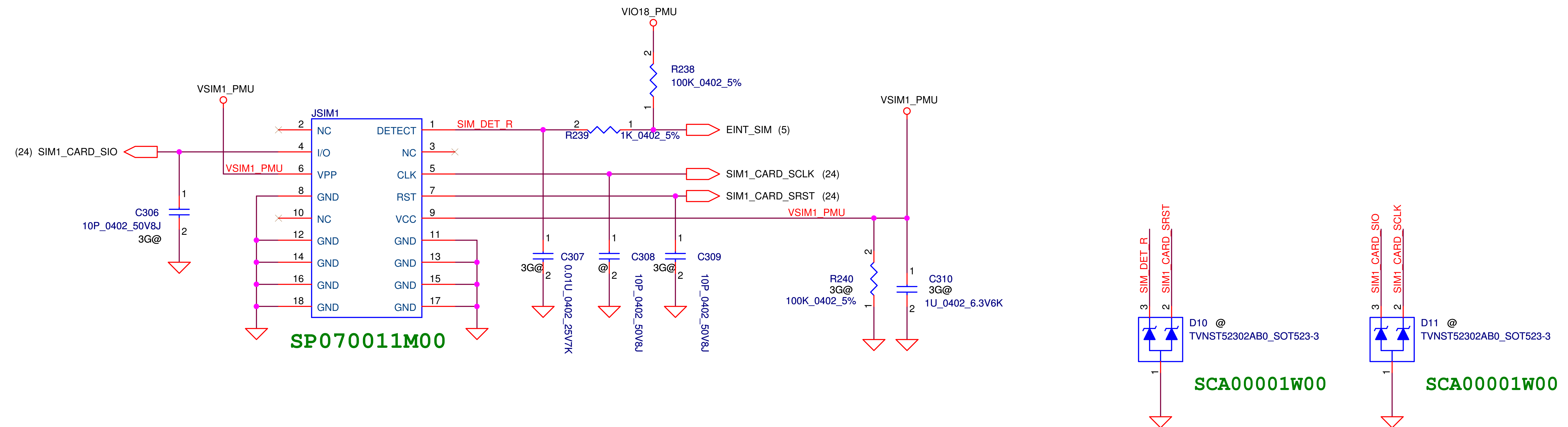
SKY13472 Truth Table

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## P sensor



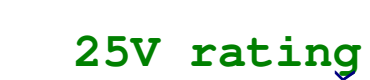
## SIM CARD



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Add BAT CONN.1102.



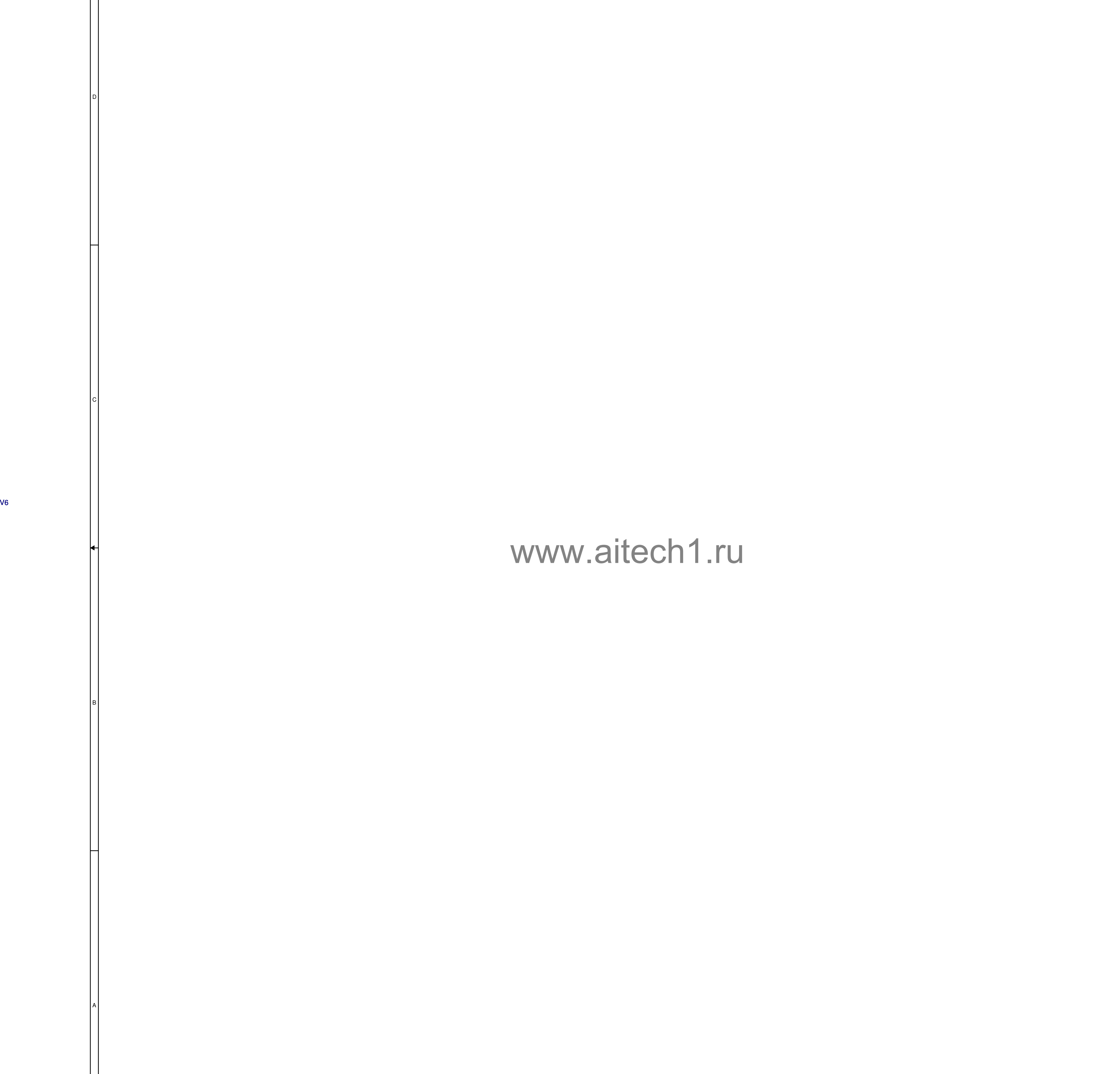
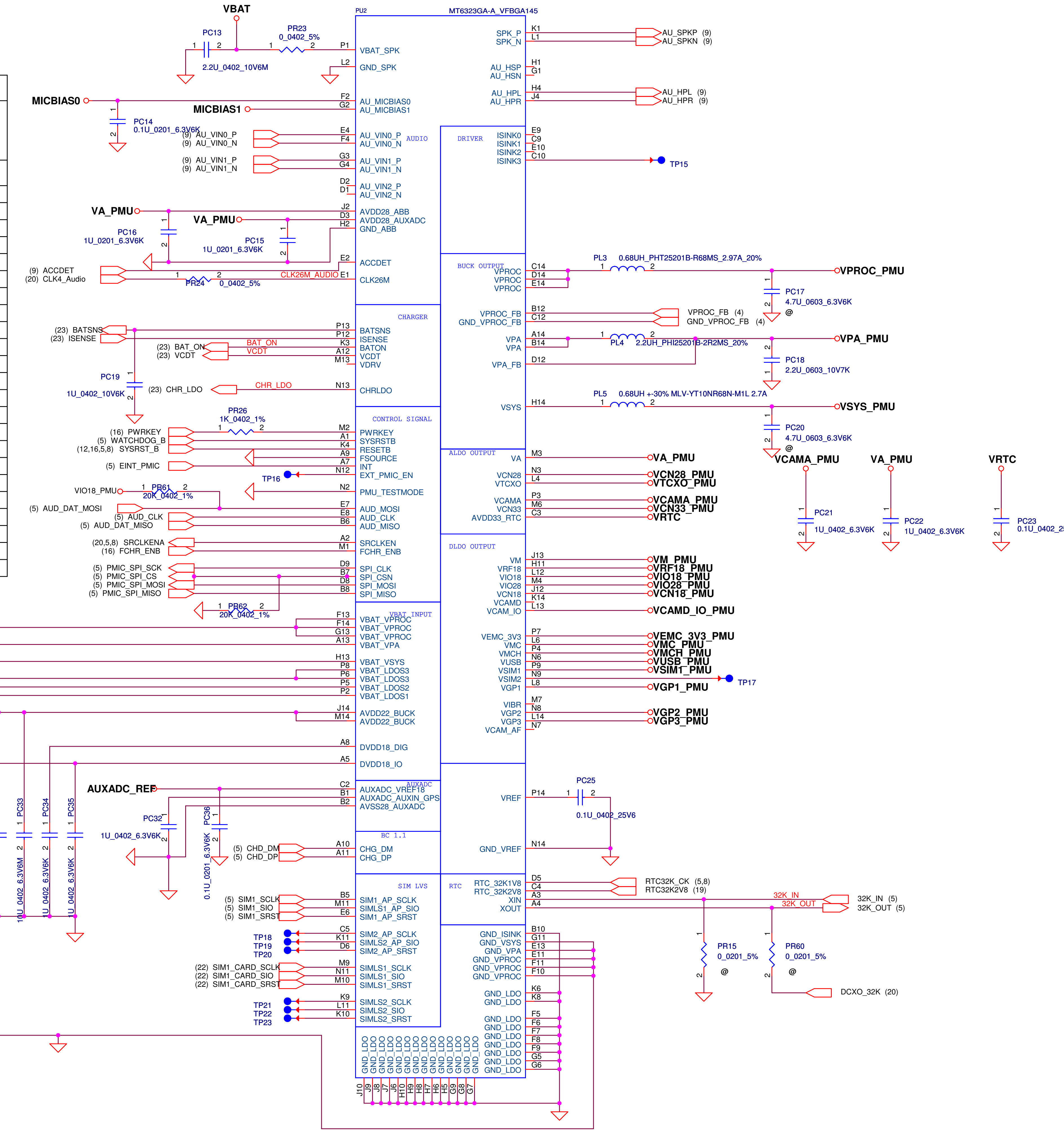
Compal Part

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			Charger	
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	LPDDR2/1.2V	PCDDR3L/1.35V	PCDDR3/1.5V	LPDDR1/1.8V	Default
SPI_CSN	H	L(20K)	H	L(20K)	PU
AUD_MOSI	L	H(20K)	H(20K)	L	PD

PMIC

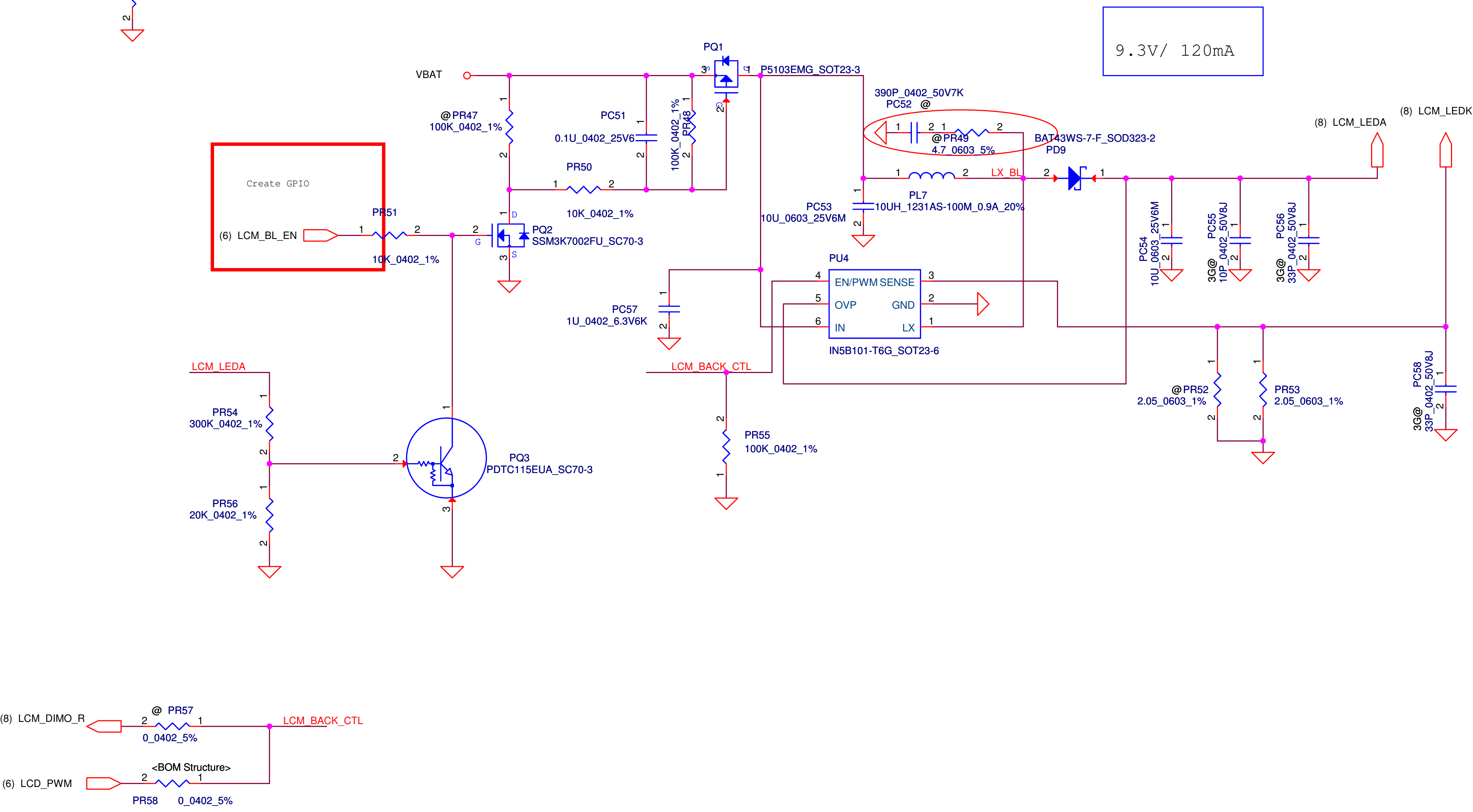
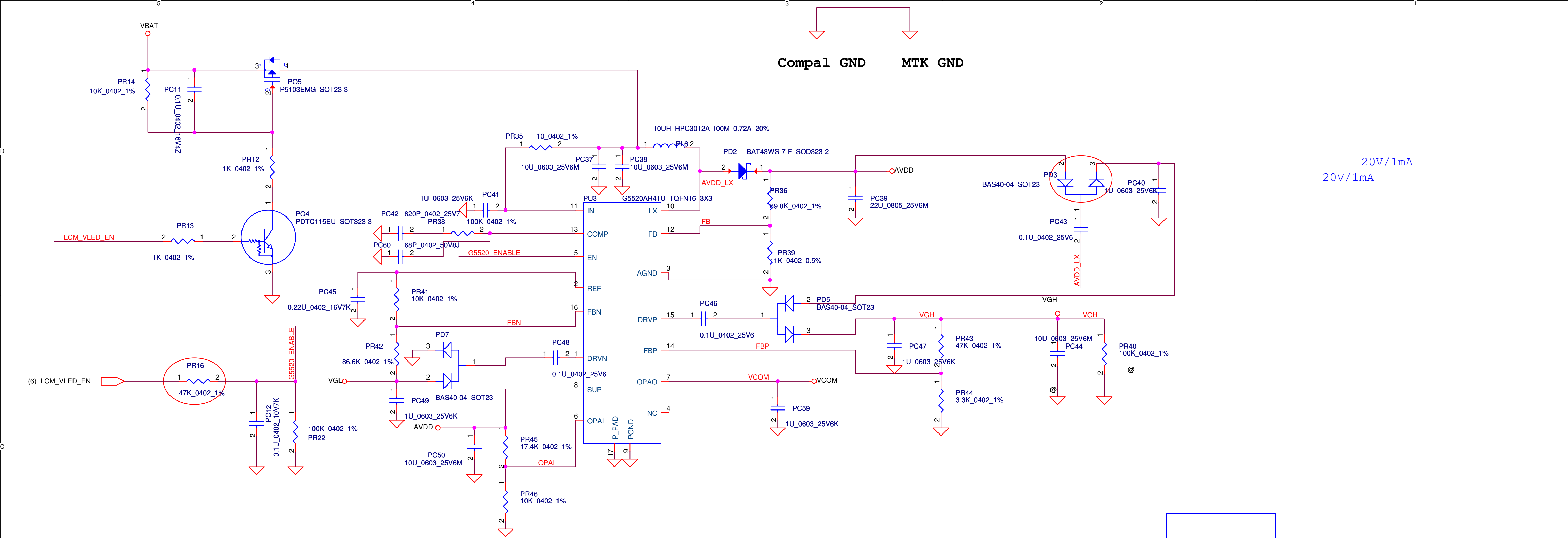
	Symbol	Vout (V)	Iout (mA)
Buck	VPROC	1.15	2800
	VPA	0.5-3.4	600
	VSYS	2.2	1200
Analog LDO	VA	2.8	150
	VCN28	2.8	30
	VTCXO	2.8	40
	VCAMA	2.8	150
	VCN33	3.3/3.4/3.5/3.6	240
Digital LDO	VRTC	2.8	2
	VM	1.24/1.35/1.5/1.84	700
	VRF18	1.825	200
	VIO18	1.8	300
	VIO28	2.8	200
	VCN18	1.8	120
	VCAMD	1.2/1.3/1.5/1.8	150
	VCAM_IO	1.8	100
	VEMC_3V3	3.0/3.3	400
	VMC	1.8/3.3	100
	VMCH	3.0/3.3	400
	VUSB	3.3	20
	VSIM1	1.8/3.0	50
	VSIM2	1.8/3.0	50
	VGP1	1.2/1.3/1.5/1.8/2.0/2.8/3.0/3.3	100
	VIBR	1.2/1.3/1.5/1.8/2.0/2.8/3.0/3.3	100
	VGP2	1.2/1.3/1.5/1.8/2.0/2.5/2.8/3.0	200
	VGP3	1.2/1.3/1.5/1.8	200
	VCAM_AF	1.2/1.3/1.5/1.8/2.0/2.8/3.0/3.3	100



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2013/3/11  
Update PD11 , D308 , D309 , D310 , D311 , L301 ,  
L302 , L303 , L304 , L307 , C647 , C646 , C635 ,  
C638 compal P/N  
Change to un-pop : PR50 , R1838 , R314 ,R315  
Change to pop : R1855

2013/3/13  
Add C318

2013/3/14  
Del R332,R348

2013/4/1  
add R1847  
reserve 0-ohm for panel design

2013/4/10  
reserve C346 for VGP6 PMU  
PR51 change to 15K\_0402\_1%  
PH1 change to 10K\_0402\_1%

2013/4/15  
Add P15 , PC143 ,PC144 for EMI request  
Add PC21 , PC22 ,PC23 for RF request

2013/4/22  
Change L307 and C342 to reserve

2013/5/6  
Change L306 and C338 BOM structure to 3G8

2013/5/6  
Change PC21,PC22 ,and PC23 BOM structure to 3G8  
PC21 change from 22P to 10P  
PC22 and PC23 change 68P to 33P

2013/5/8  
Change L305 and C337 BOM structure to 3G8

2013/5/8  
Change D311 and C638 BOM structure to 3G8

2013/5/20  
Change PR30 , PR31 ,R339 ,R321 , R322 , R323  
R324 , R325 , R326 , R327 , R328, R329 , R338 ,  
R1855 to R-SHORT

2013/5/24  
Add PR32(reserve)

2013/6/18  
L304 change from SH0000XK00 to SH000000U00  
L301,L302,L303 change from SH0000XJ00 to SH000000YN00

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POWER PIR

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